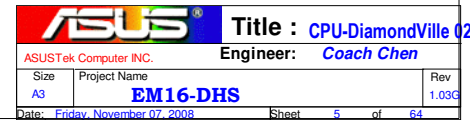


BOM OK.

BOM OK.



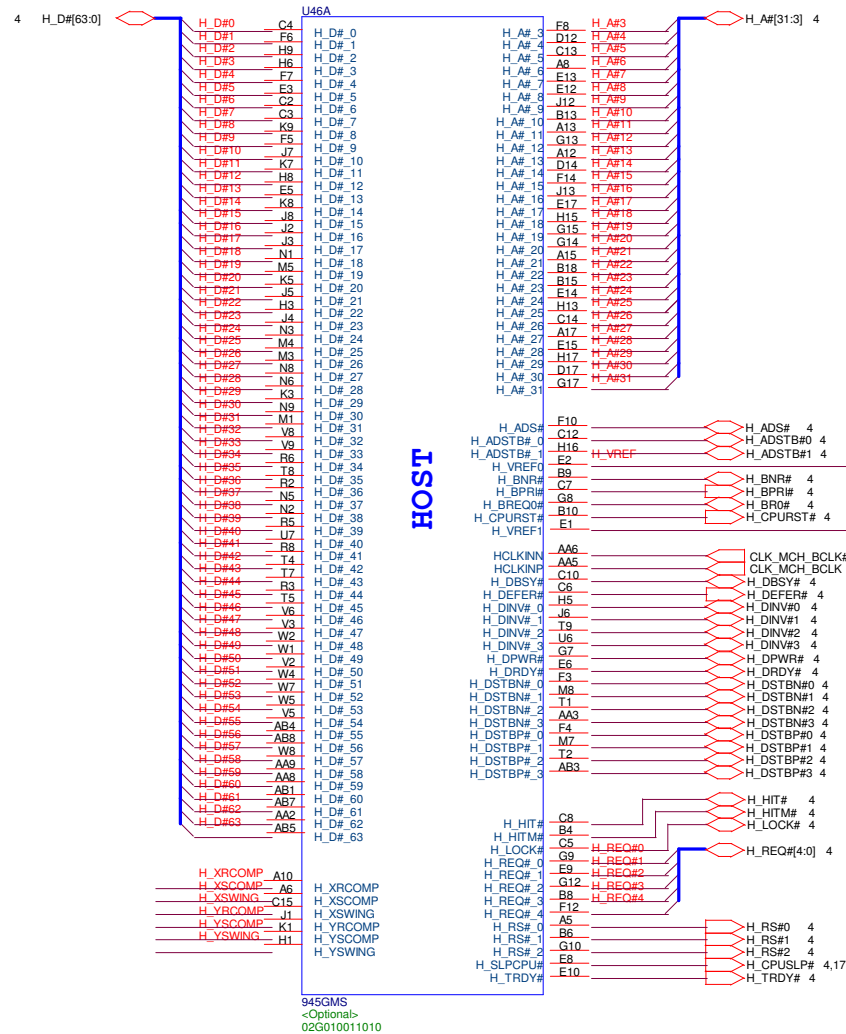
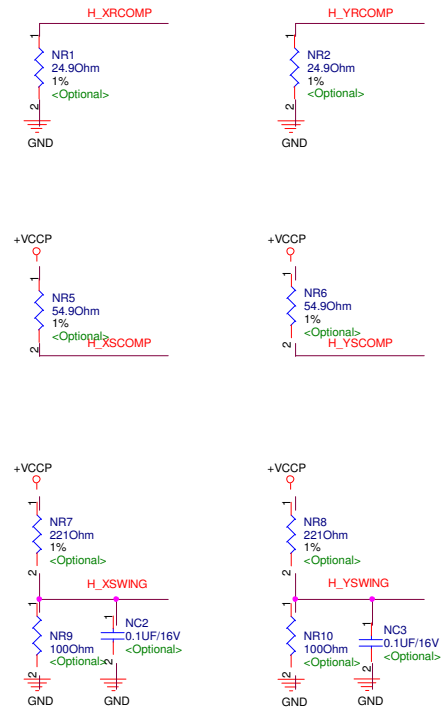
BOM OK.



# NB-945GMS(HOST)

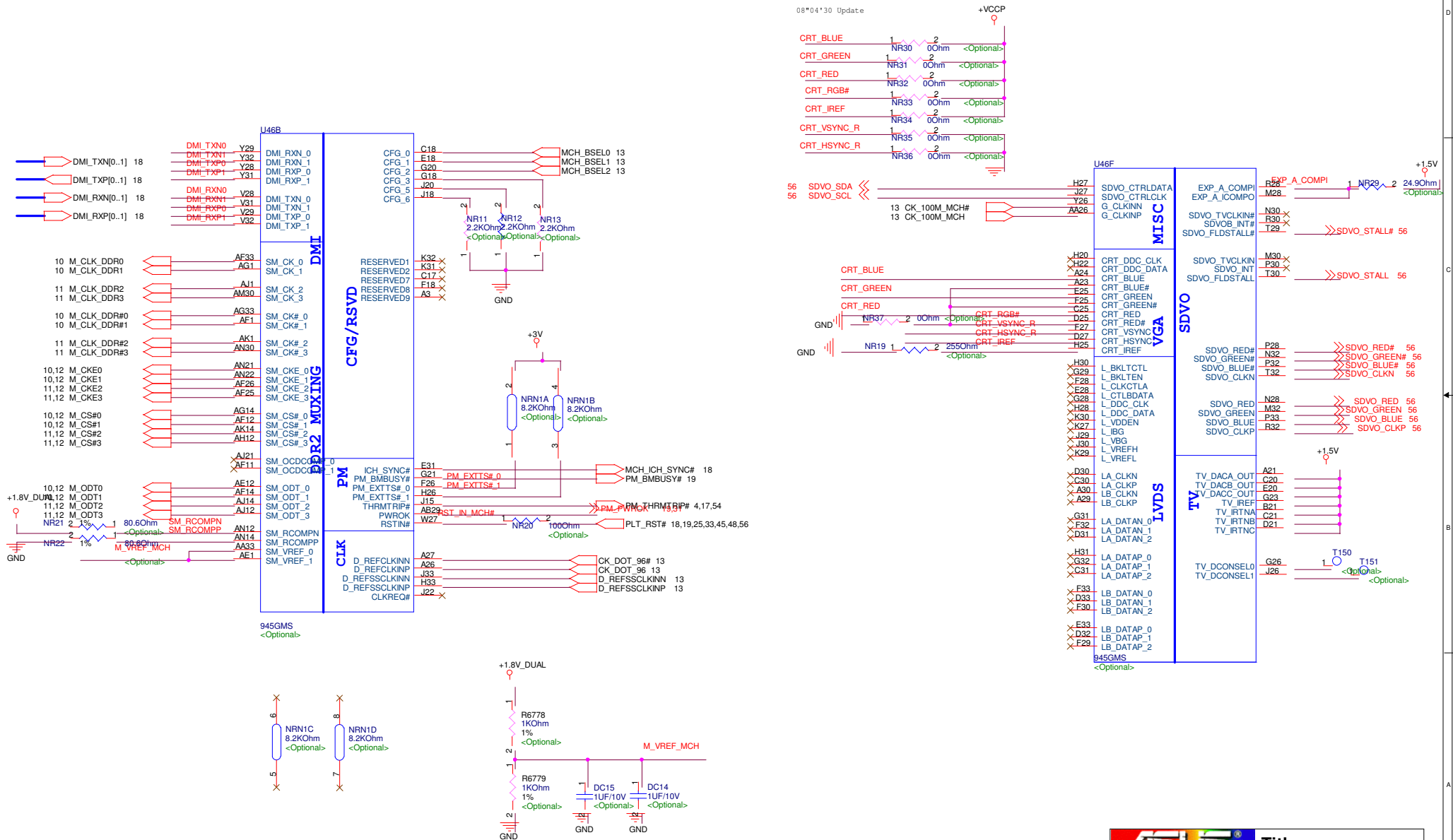
BOM OK.

P/N: 02G010018202



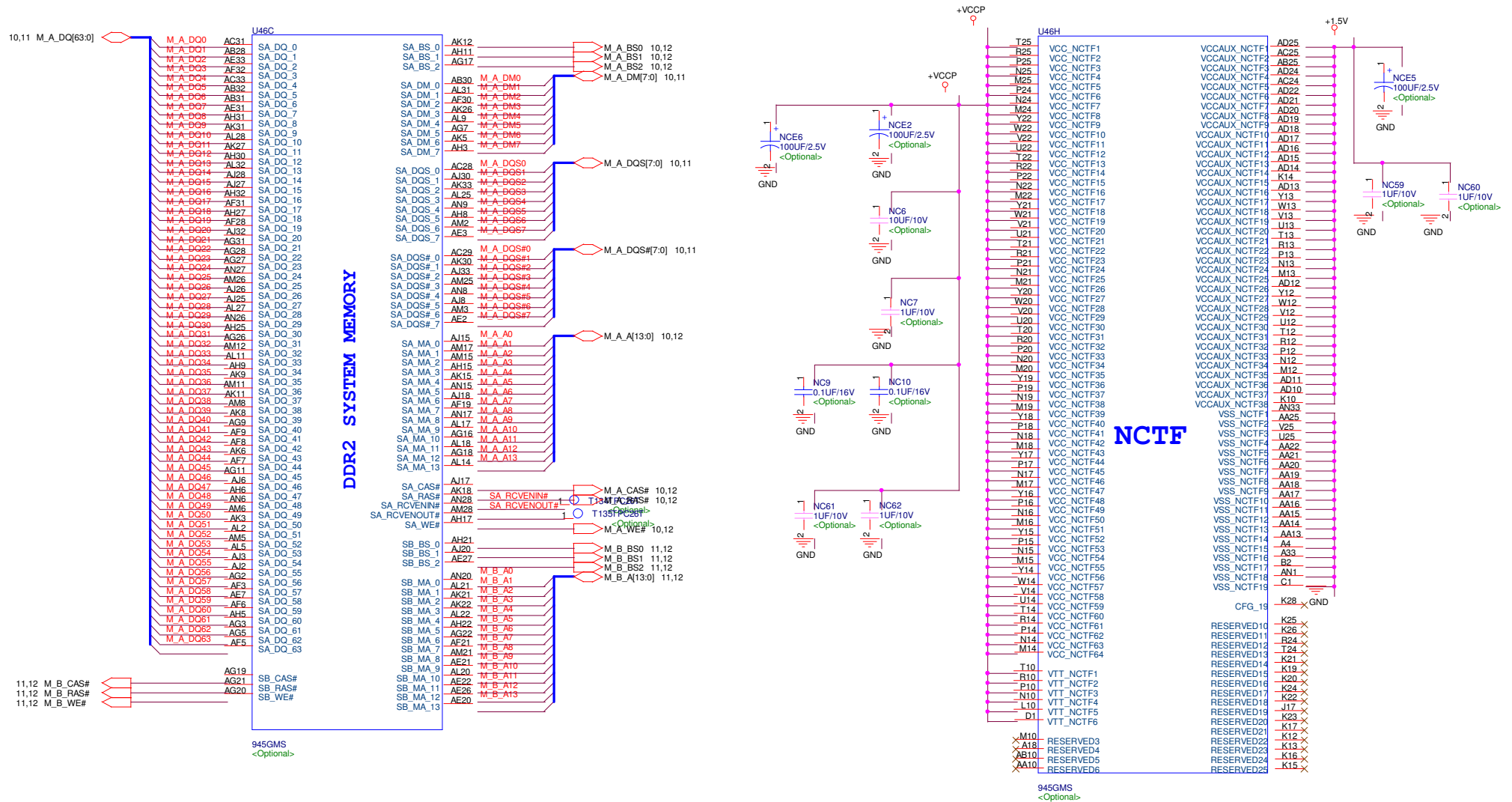
# NB-945GMS(DMI/CFG/GRAPHIC)

BOM OK.



# NB-945GMS(DDR/POWER)

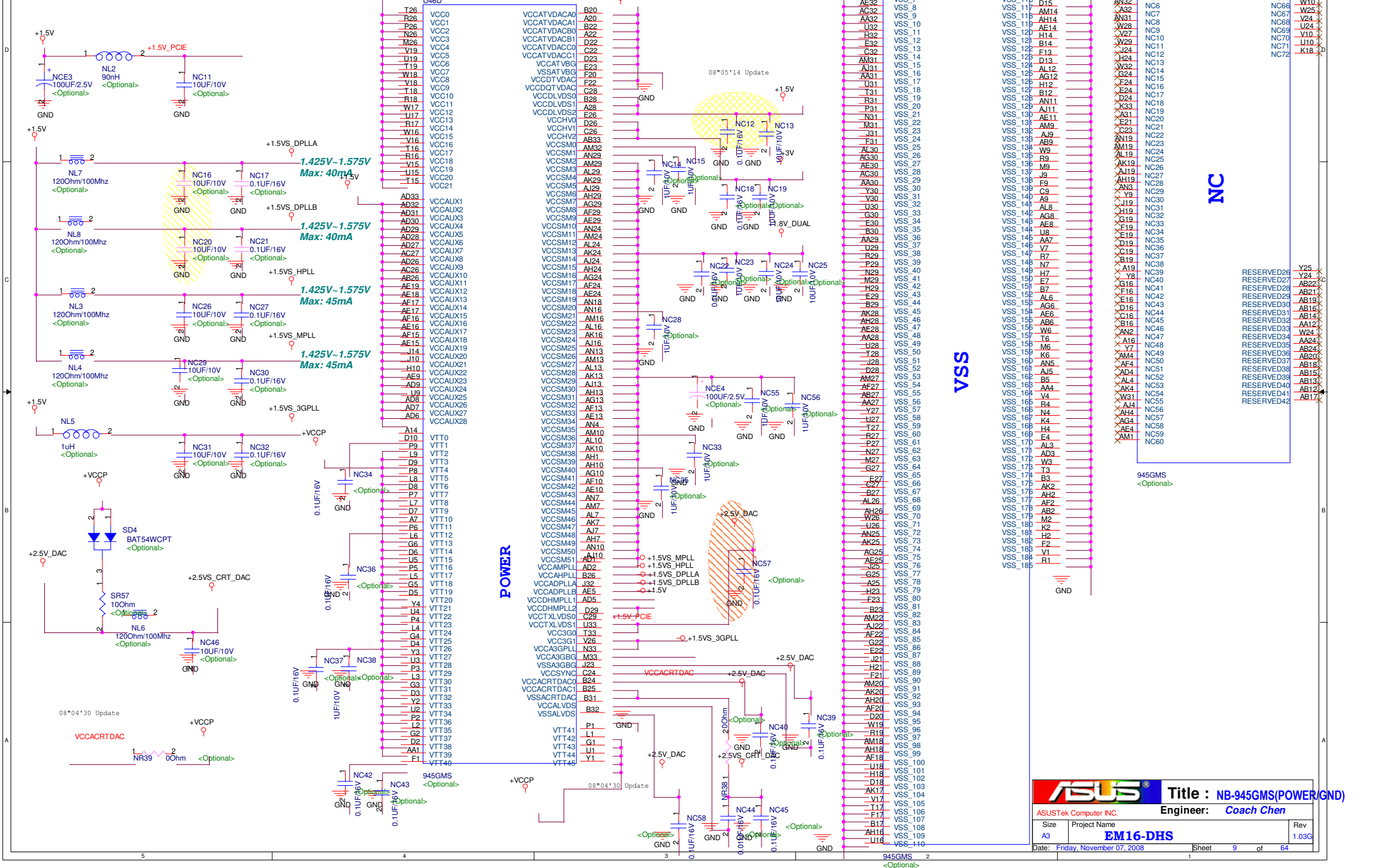
BOM OK.





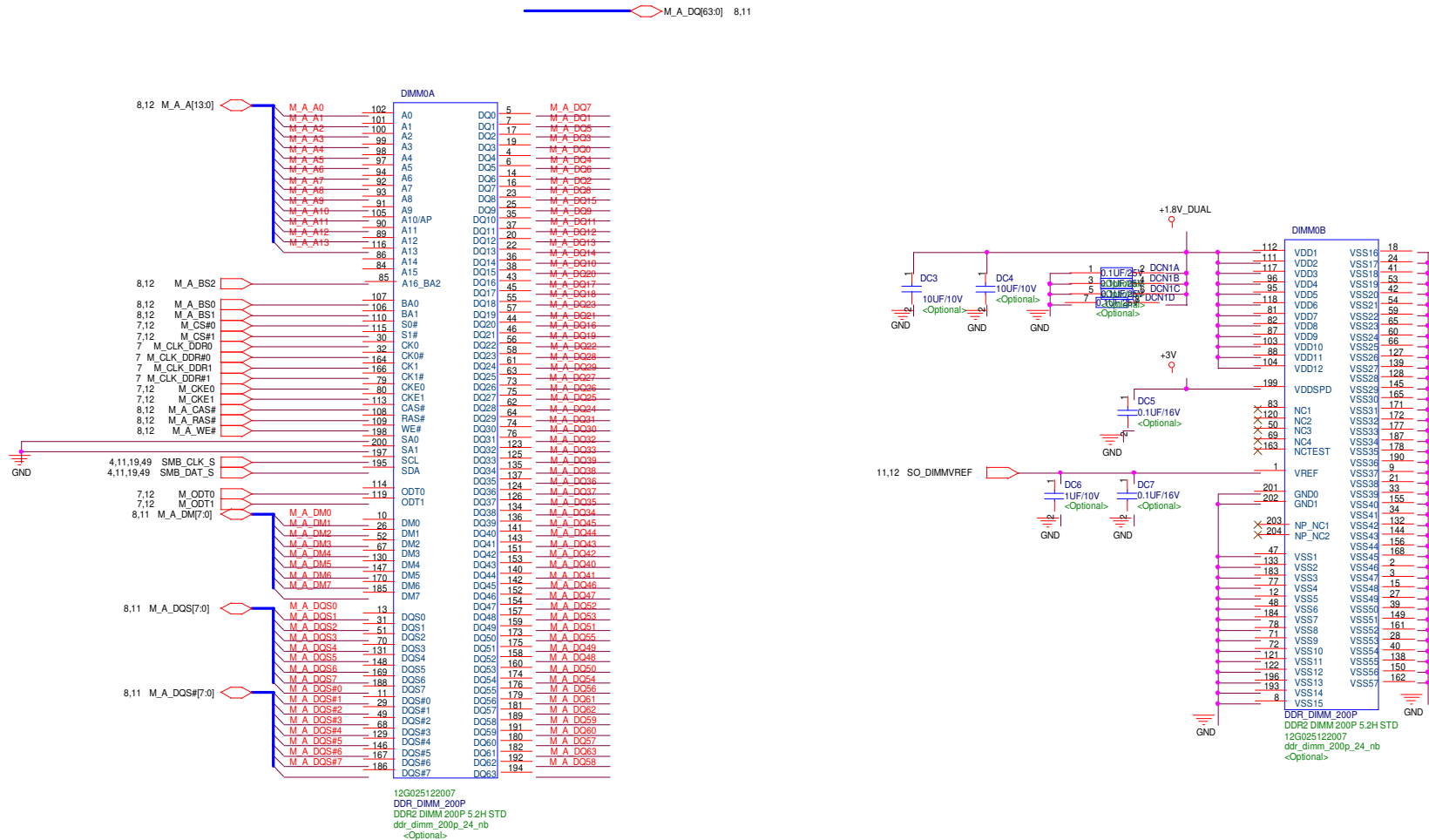
# NB-945GMS(POWER/GND)

BOM OK.



# DDR2 SO-DIMM0

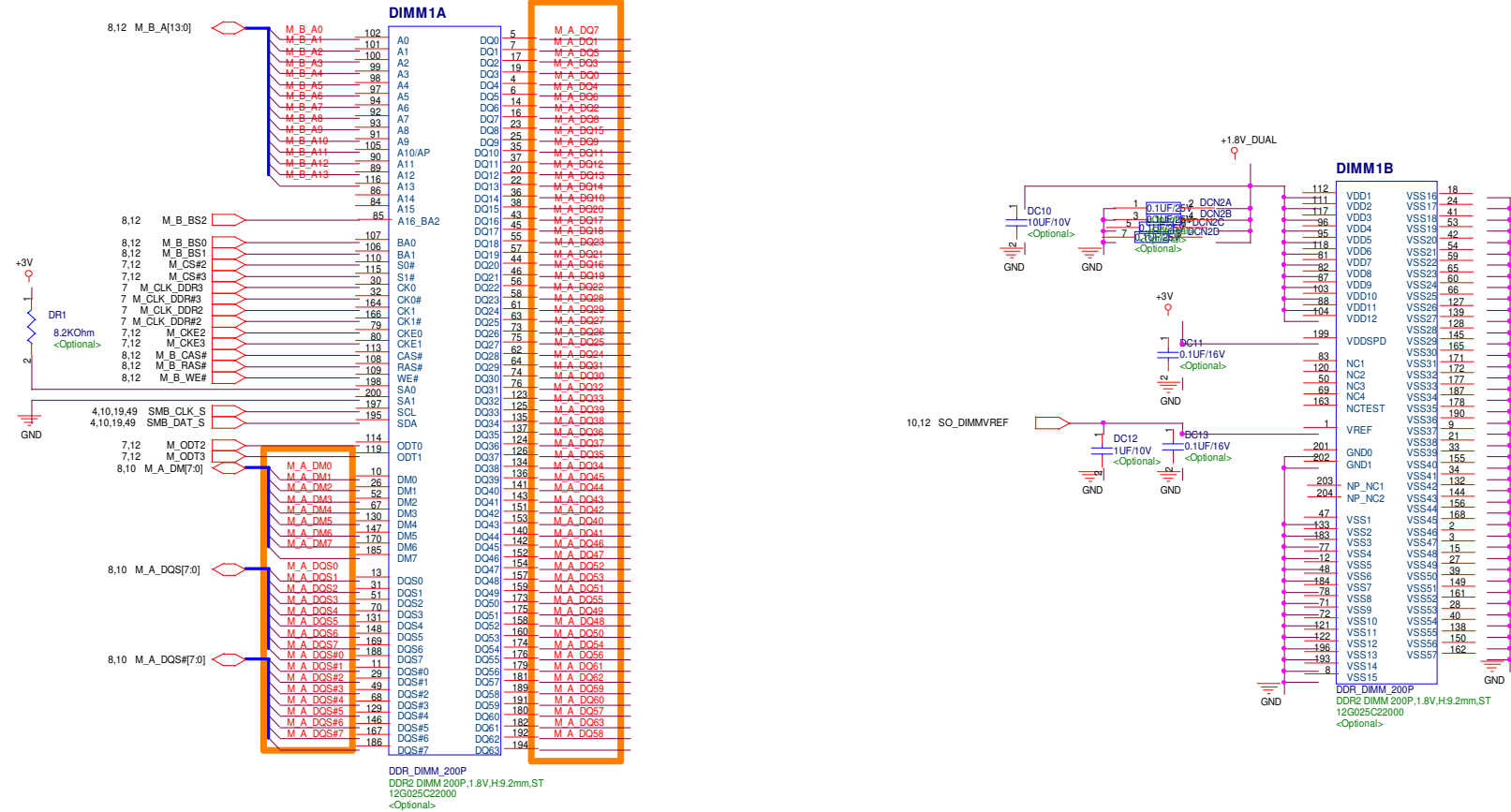
BOM OK.



# DDR2 SO-DIMM1

BOM OK.

M\_A\_DQ[63:0] 8,10

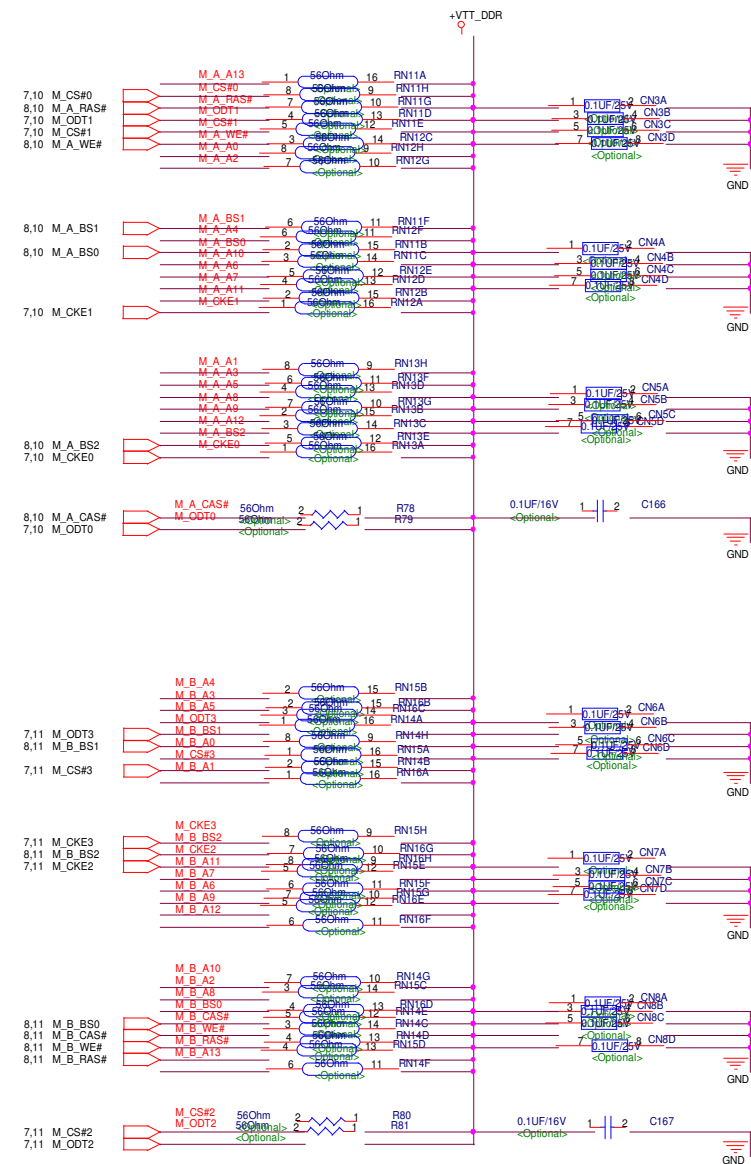




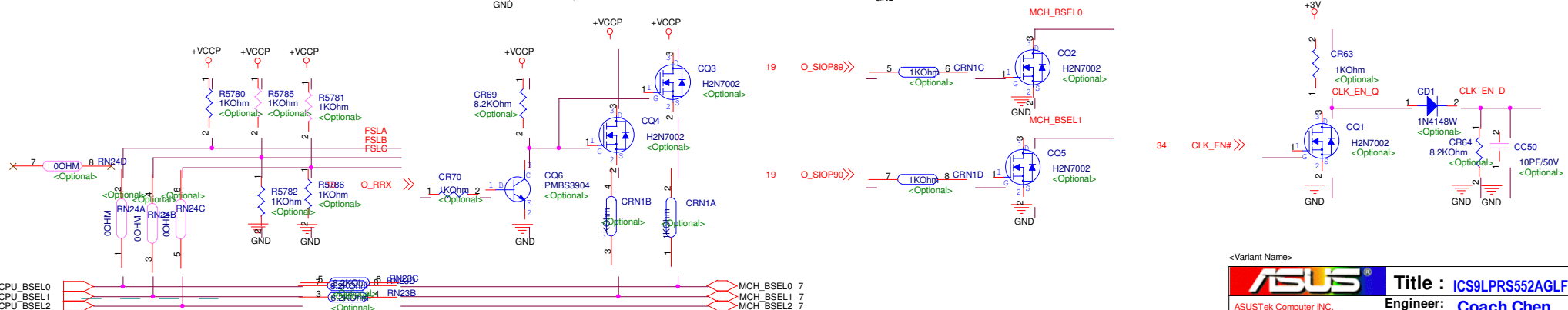
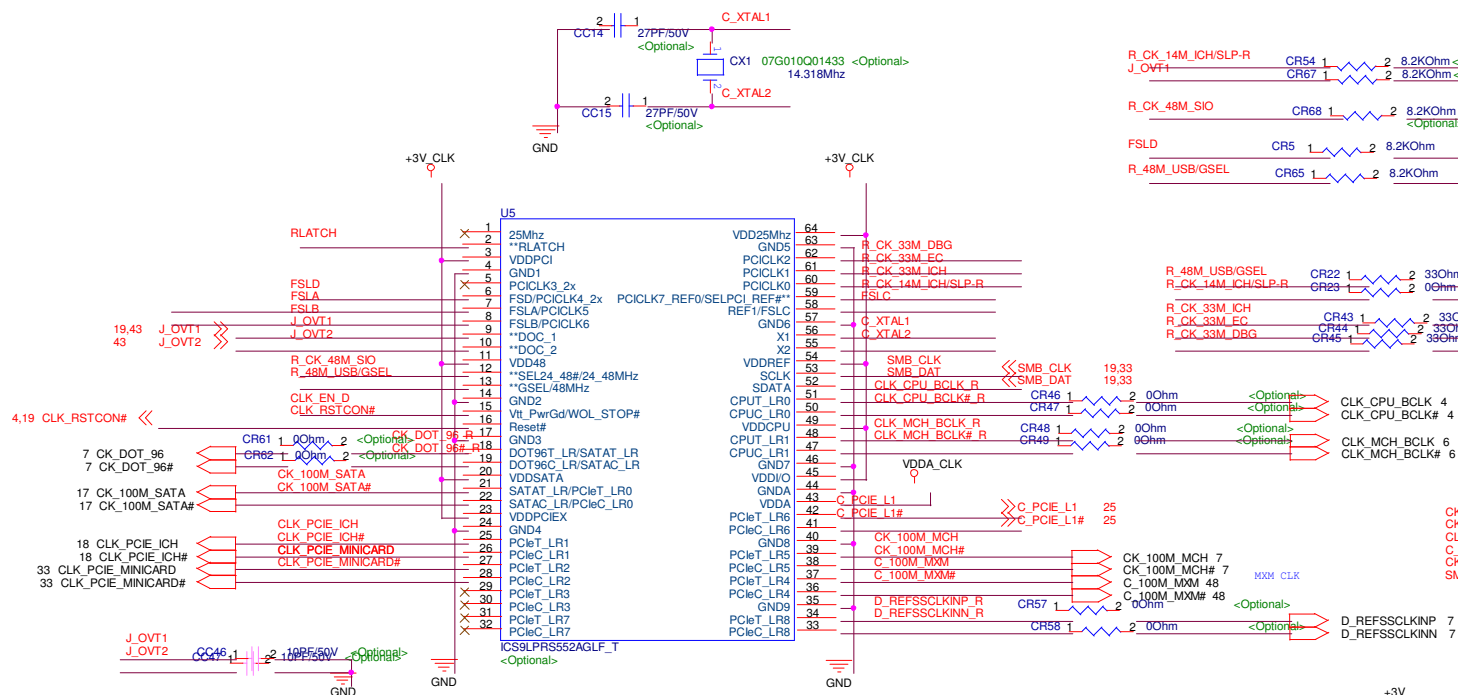
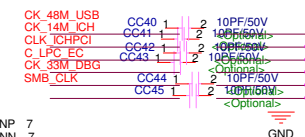
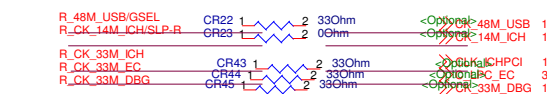
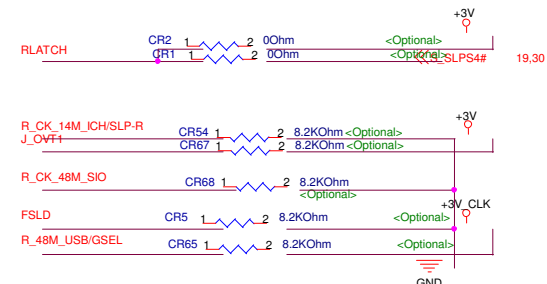
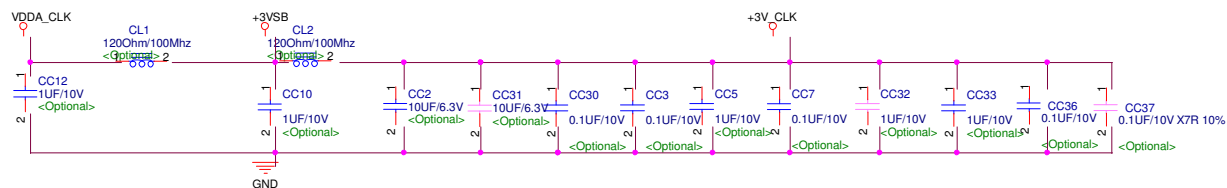
# DDR2 TERMINATION

BOM OK.

8,10 M\_A\_A[13:0]  
8,11 M\_B\_A[13:0]



BOM OK.



BOM OK.

BOM OK.



BOM OK.



BOM OK.



BOM OK.




BOM OK.

BOM OK.

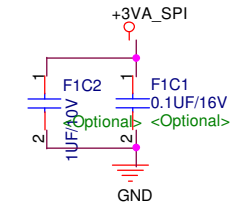
BOM OK.



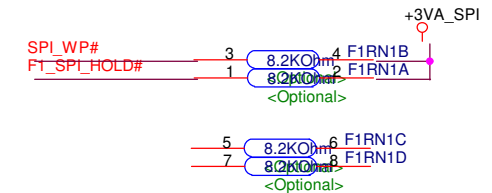
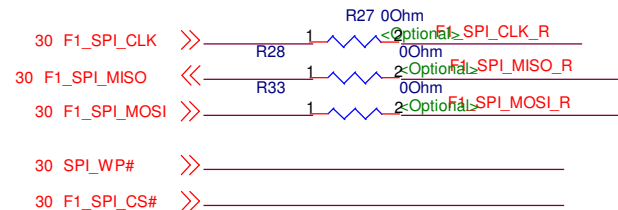
**NB-945GMS VGA Port**

		<b>Title :</b> NB-945GMS VGA Port	
ASUSTek Computer INC.		<b>Engineer:</b> Coach Chen	
Size A3	Project Name <b>EM16-DHS</b>		Rev 1.03G
Date: Friday, November 07, 2008		Sheet	15 of 64

**BOM OK.**



17,30 LPC\_AD[0:3]◀▶



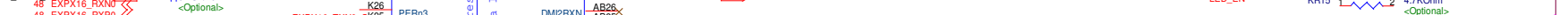


BOM OK.





R102G

























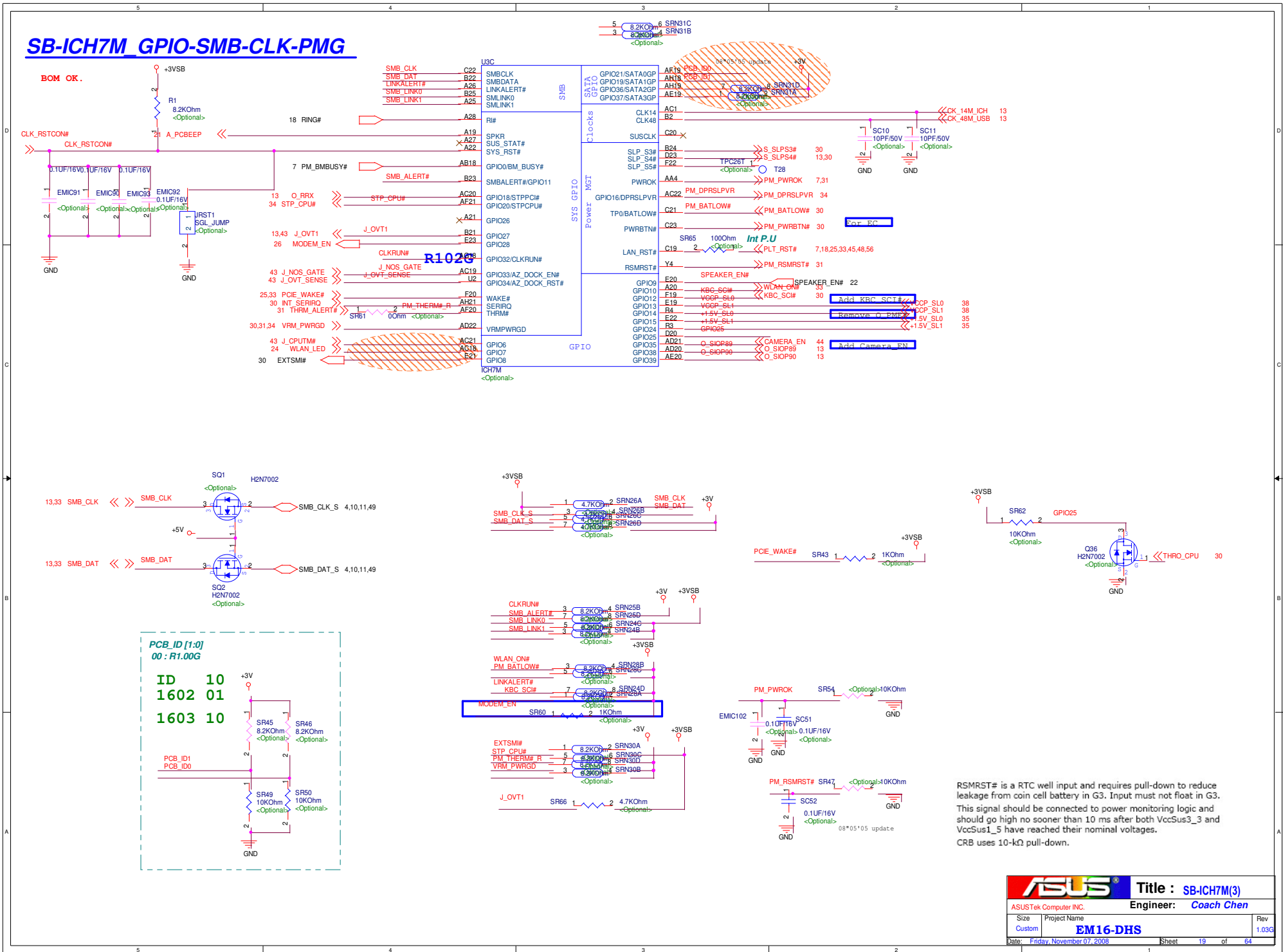






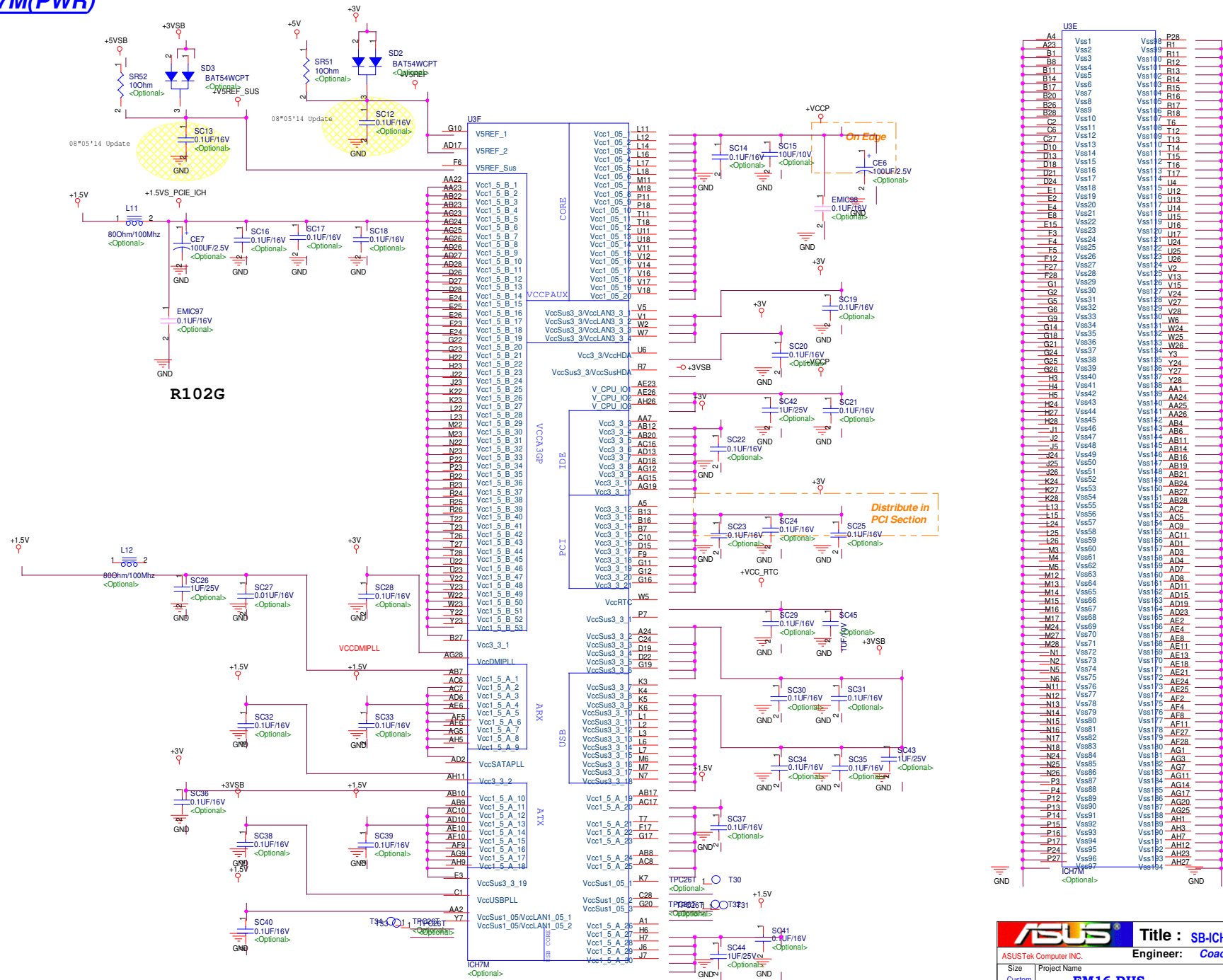



## SB-ICH7M\_GPIO-SMB-CLK-PMG



**SB-ICH7M(PWR)**

**BOM OK.**

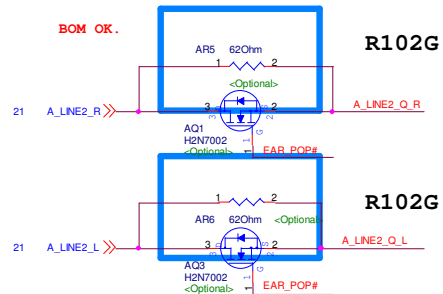




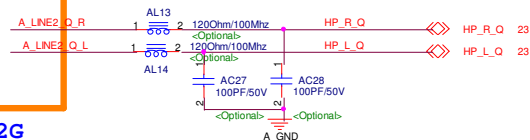


# ALC888-CODEC-2

BOM OK.

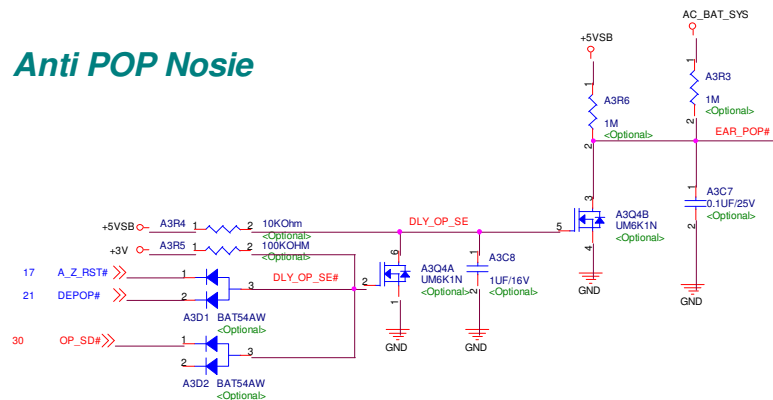


R102G

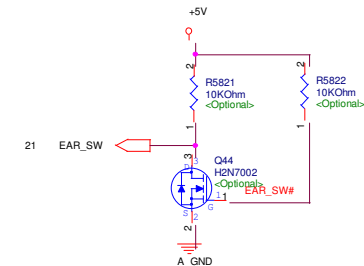
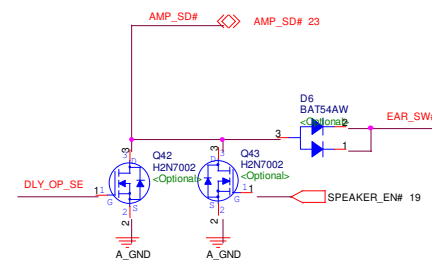


CHECK

## Anti POP Nosie



CHECK



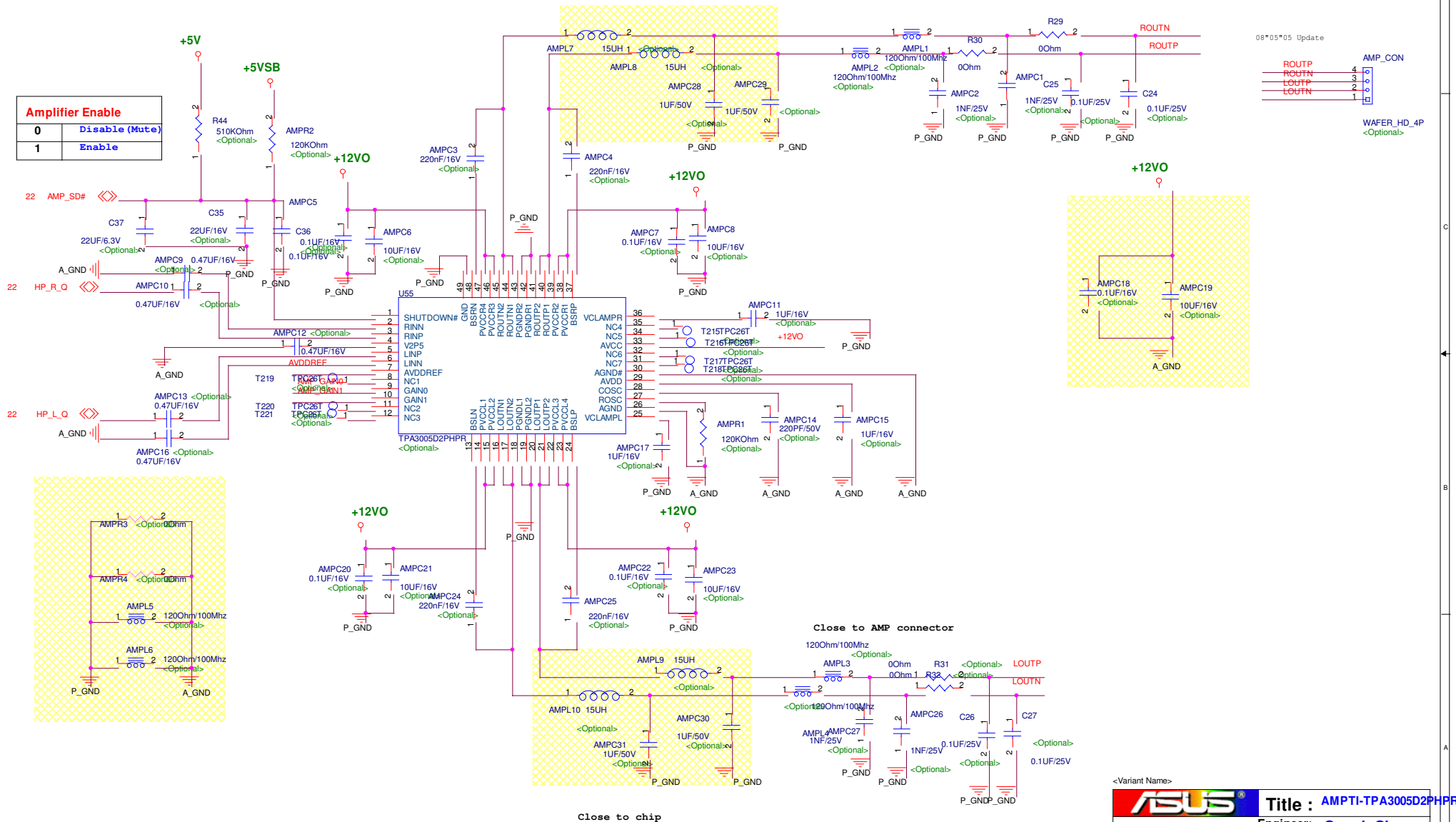
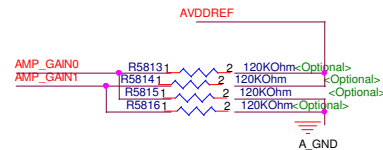
<Variant Name>		Title : ALC888-CODEC-2	
ASUSTek Computer INC.		Engineer: Coach Chen	
Size	Project Name	Rev	Rev
Custom	EM16-DHS	1.03G	1.03G
Date: Friday, November 07, 2008	Sheet 22 of 64		

# AMP TI-TPA3005D2PHPR

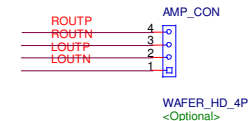
BOM OK.

Amplifier Enable	
0	Disable (Mute)
1	Enable

GAIN1	GAIN0	dB	Z1 (KΩ)
0	0	15.3dB	137
0	1	21.2dB	88
1	0	27.2dB	52
1	1	31.8dB	33



08\*05\*05 Update



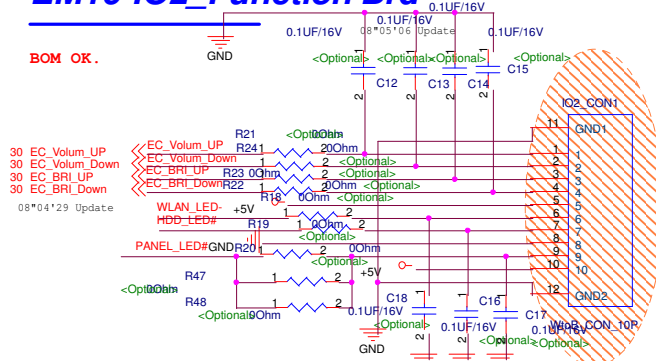
<Variant Name>

<b>ASUS</b>		Title : AMP TI-TPA3005D2PHPR	
ASUSTek Computer INC.		Engineer: Coach Chen	
Size	Project Name	EM16-DHS	
A3		Rev 1.03G	
Date: Friday, November 07, 2008		Sheet 23 of 64	

## EM16-IO2\_Function Brd

BOM OK.

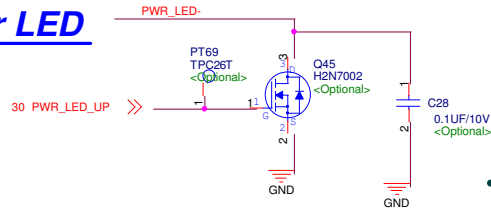
EMI ADD RC



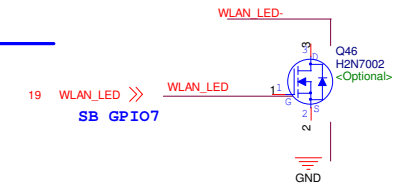
EM16-IO2\_Left Define

P/N	Pin No.	Symbol	I/O	Description
	1	1	I/O	EC_Volum_UP
	2	2	I/O	EC_Volum_Down
	3	3	I/O	EC_BRI_UP
	4	4	I/O	EC_BRI_Down
	5	5	I	+5V
	6	6	I/O	WLAN_LED-
	7	7	I/O	HDD_LED#
	8	8	I	GND
	9	9	I/O	PANEL_LED#
	10	10		NC

## Power LED

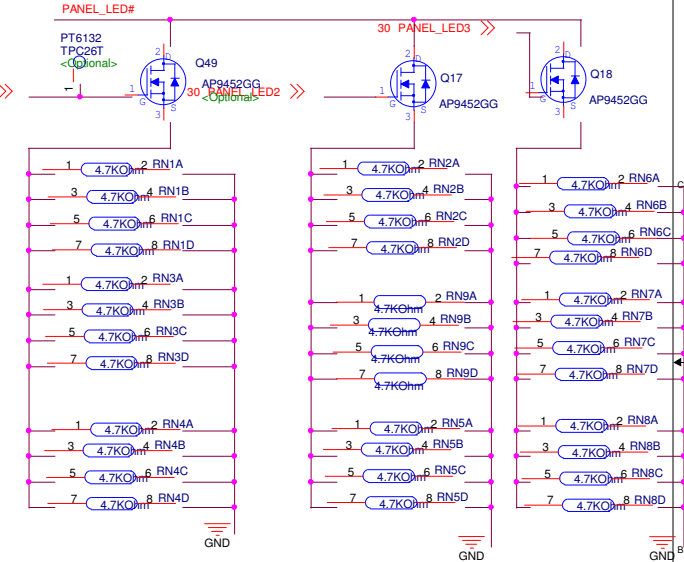
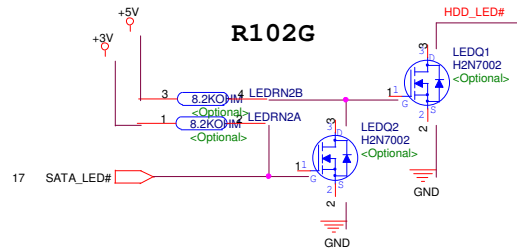


## WLAN LED



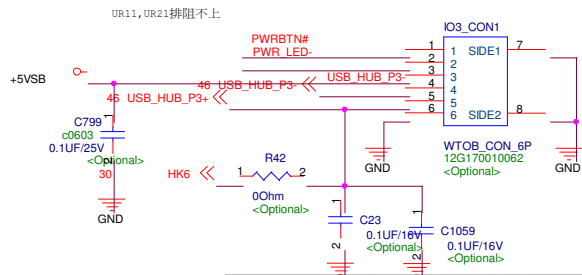
## PANEL LED

## SATA HDD LED



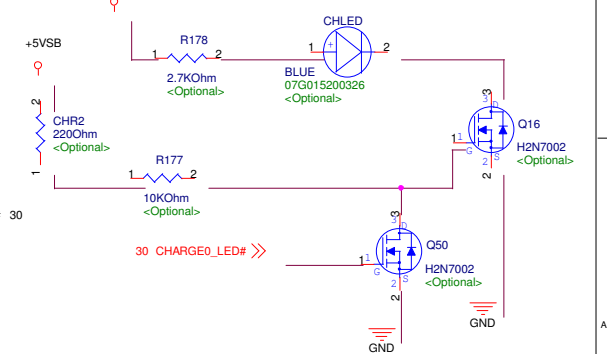
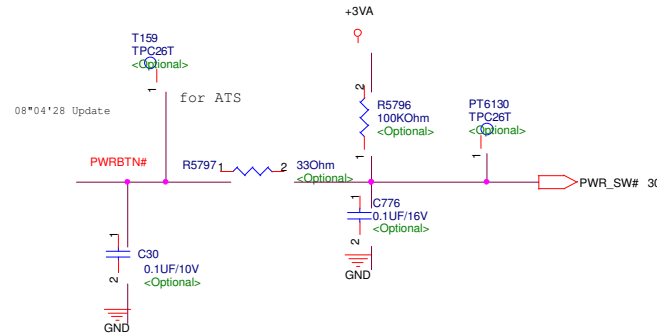
## EM16-IO3\_Power Brd

## PWR Button



EM16-IO3\_Right Define

P/N	Pin No.	Symbol	I/O	Description
	1	1	I/O	IO3_PWRBTN#
	2	2	I/O	IO3_PWR_LED-
	3	3	PWR	+5VSB
	4	4	I/O	USB D-
	5	5	I/O	USB D+
	6	6	G	Gnd



<Variant Name>

<b>ASUS</b>		Title : SWITCH/LED	
ASUSTek Computer INC.		Engineer: Coach Chen	
Size A3	Project Name EM16-DHS	Rev 1.03G	
Date: Friday, November 07, 2008	Sheet 24	of 64	

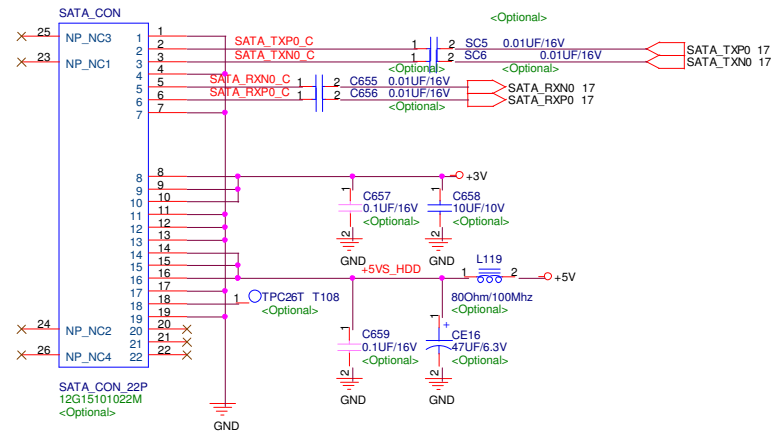
BOM OK.



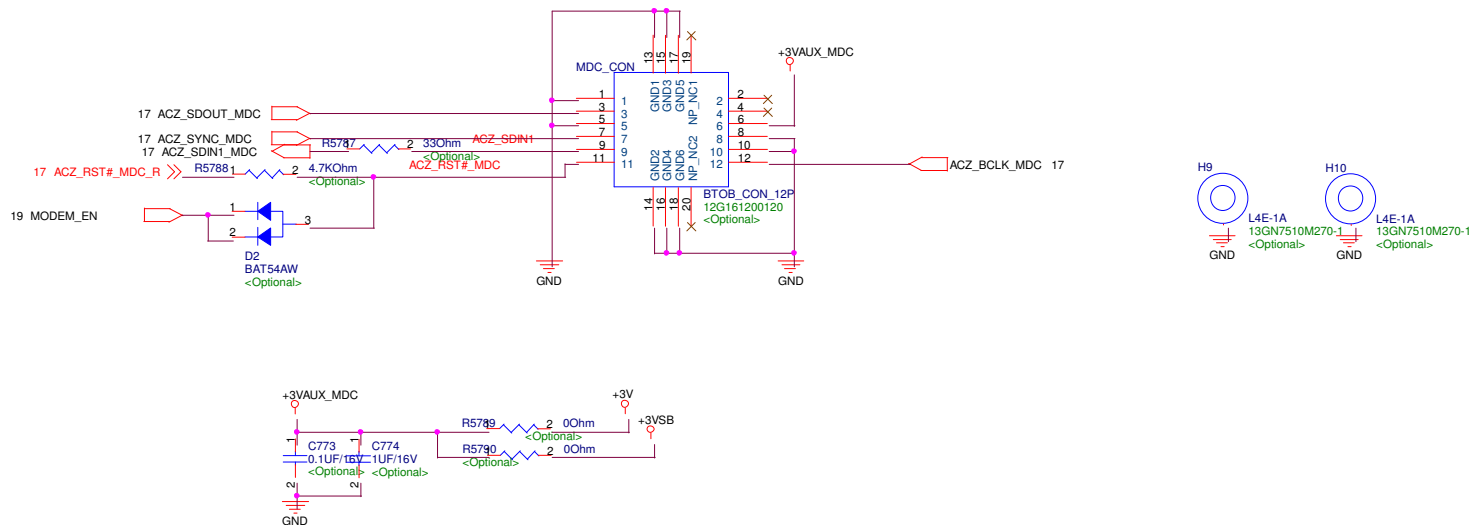
**公座**  
**P/N: 12G16020040E**

EM16-DHS IO1_CON							
P/N							
Pin No.	Symbol	I/O	Description	Pin No.	Symbol	I/O	Description
1	1	PWR	IO1_+U_USB01_5V_L	21	21	I/O	IO1_X_L1X1_RXP
2	2	PWR	IO1_+U_USB01_5V_L	22	22	PWR	IO1_+3VSB
3	3	I/O	IO1_U_USB0+_R	23	23	G	GND
4	4	I/O	IO1_U_USB1+_R	24	24	PWR	IO1_+3VSB
5	5	I/O	IO1_U_USB0+_R	25	25	PWR	IO1_+U_USB23_5V_L
6	6	I/O	IO1_U_USB1-_R	26	26	I/O	IO1_A_JD_LINE
7	7	G	GND	27	27	I/O	IO1_U_USB3-_R
8	8	G	GND	28	28	I/O	IO1_A_LINE_R
9	9	I/O	IO1_C_PCIE_L1	29	29	I/O	IO1_U_USB3+_R
10	10	I/O	IO1_U_USB2+_R	30	30	I/O	IO1_A_LINE_L
11	11	I/O	IO1_C_PCIE_L1#	31	31	G	AGND
12	12	I/O	IO1_U_USB2+_R	32	32	G	AGND
13	13	G	GND	33	33	PWR	IO1_+3V
14	14	PWR	IO1_+U_USB23_5V_L	34	34	I/O	IO1_A_JD_LOUT
15	15	I/O	IO1_X_L1X1_TXN	35	35	I/O	IO1_A_JD_MIC1
16	16	I/O	IO1_PCIE_WAKE#	36	36	I/O	IO1_A_JD_LOUT
17	17	I/O	IO1_X_L1X1_TXP	37	37	I/O	IO1_A_MIC1_L
18	18	I/O	IO1_PLT_RST#	38	38	I/O	IO1_A_LOUT_L
19	19	I/O	IO1_X_L1X1_RXN	39	39	G	IO1_A_MIC1_R
20	20	PWR	IO1_+3VSB	40	40	G	AGND

BOM OK.



**BOM OK.**





# USB\_DUAL,Port 0~5

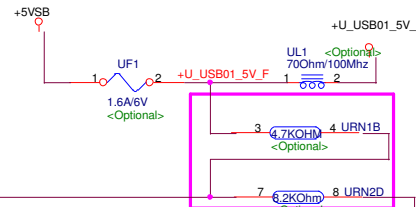
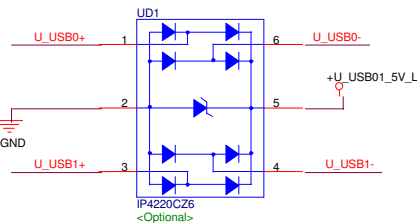
0.3C Beta

OK

BOM OK.

Rear  
USB1/USB2

Port 1,2

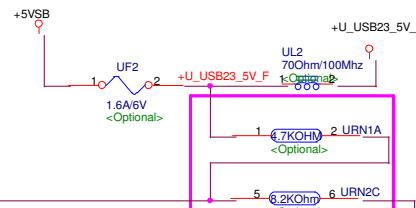
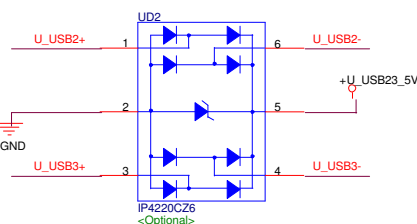


USB 0	USB1 For DHS-IOx	Rear
USB 1	USB2 For DHS-IOx	Rear
USB 2	USB3 For DHS-IOx	Rear
USB 3	USB4 For DHS-IOx	Rear
USB 4	USB5 For DHS-IOx	Front-Left
USB 5	USB6 For DHS-IOx	Front-Left
USB 6	USB7 For DHS-IOx	USB HUB
USB 7	USB8 For DHS-IOx	WebCam

Connect to Chipset

Rear  
USB3/USB4

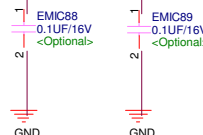
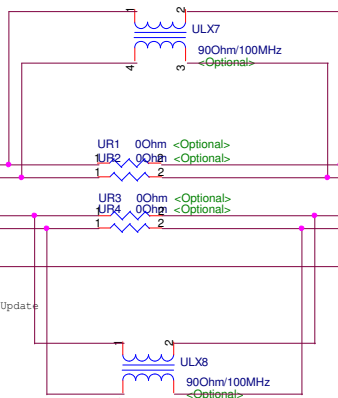
Port 3,4



Front Left Side  
USB5/USB6

18 U\_USB4+  
18 U\_USB4-  
18 U\_USB5+  
18 U\_USB5-  
18 U\_USB0C#45

08\*04\*28 Update



<Variant Name>

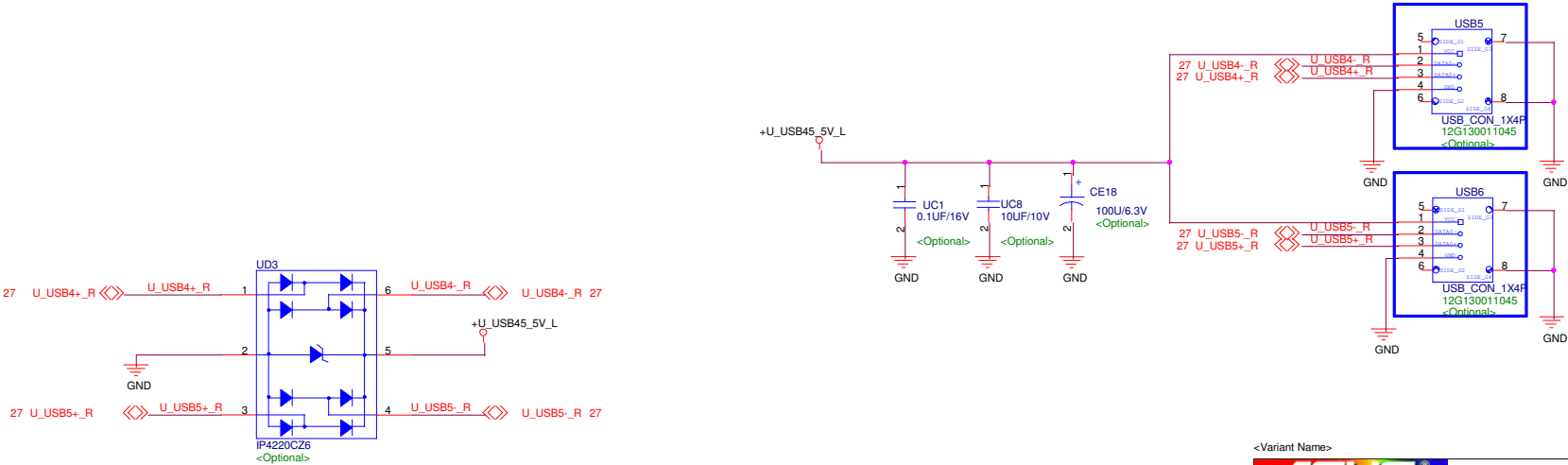
ASUS®		Title : USB_DUAL,Port 0~5 (OC+Choke)	
ASUSTek Computer INC.		Engineer: Coach Chen	
Size	Project Name	Rev	
A3	EM16-DHS	1.03C	
Date: Friday, November 07, 2008	Sheet	27	of 64

USB\_DUAL (ESD)

BOM OK.

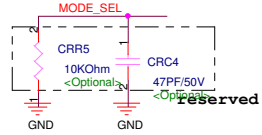
USB 0	USB1 For DHS-IOx	Rear
USB 1	USB2 For DHS-IOx	Rear
USB 2	USB3 For DHS-IOx	Rear
USB 3	USB4 For DHS-IOx	Rear
USB 4	USB5 For DHS-IOx	Front-Left
USB 5	USB6 For DHS-IOx	Front-Left
USB 6	USB7 For DHS-IOx	USB HUB
USB 7	USB8 For DHS-IOx	WebCam

Port 5,6

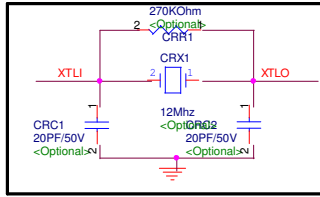


# CARD READER RTS5158E-GR

BOM OK.

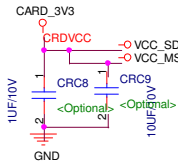
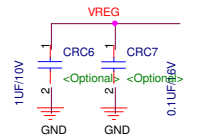
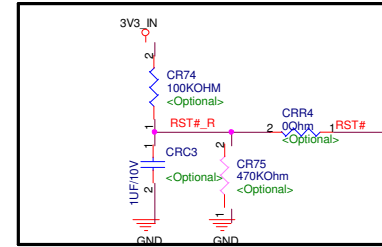


## 12Mhz

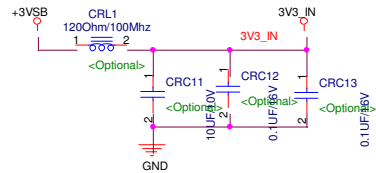


08\*04\*29 Update

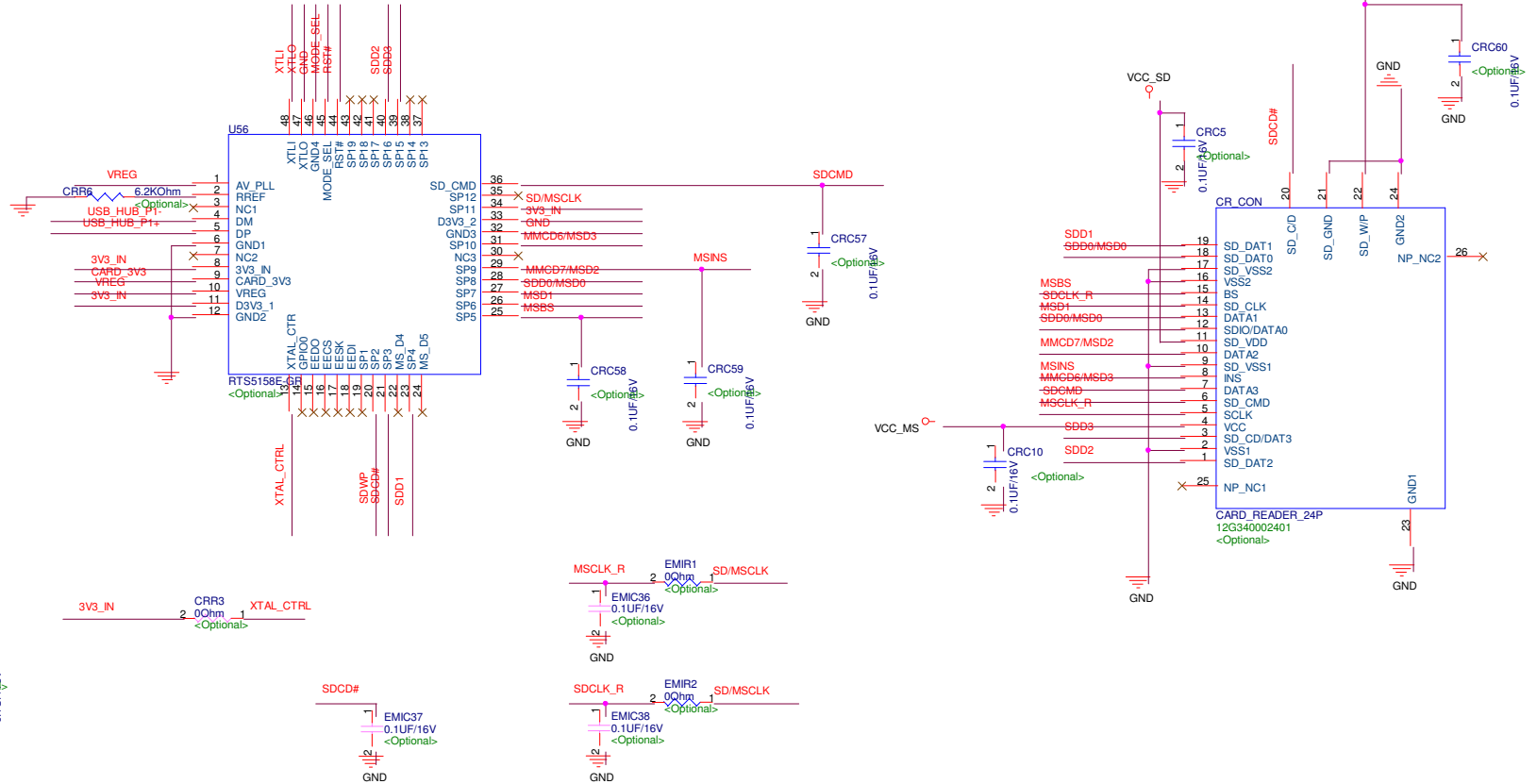
## RESET



## R102G

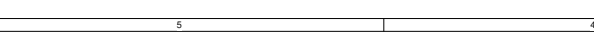
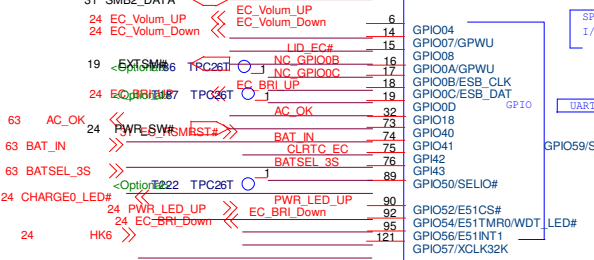
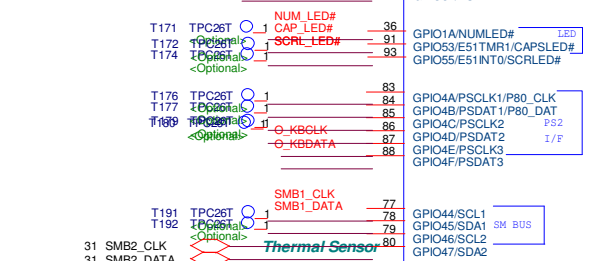
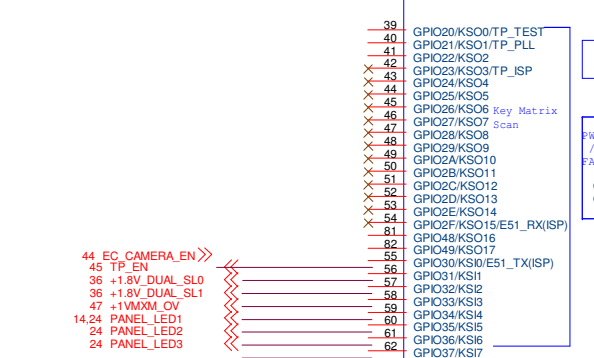
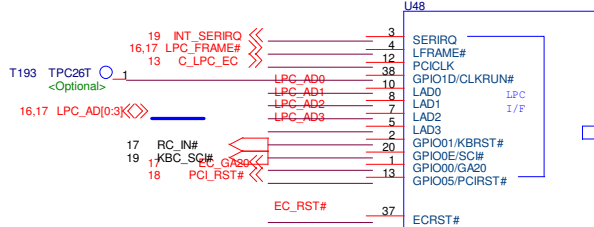
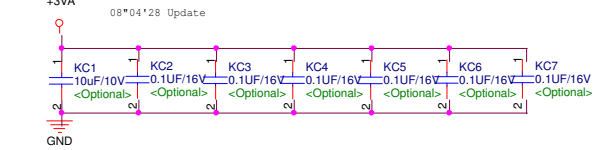


## USB HUB GL850A

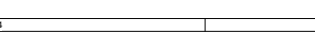
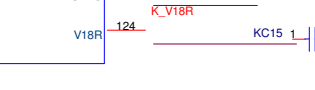
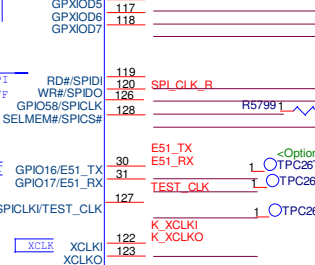
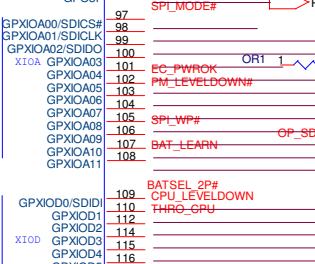
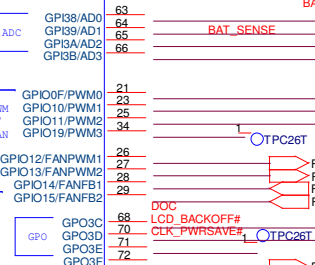
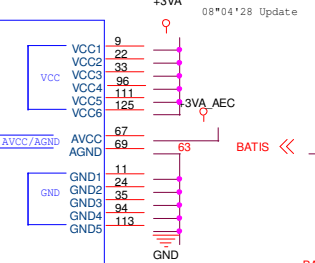
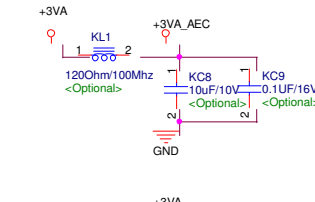


# ENE KB3310

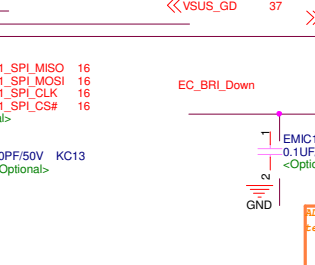
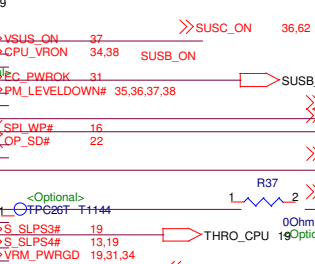
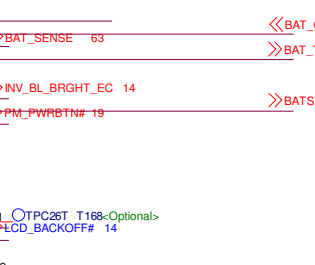
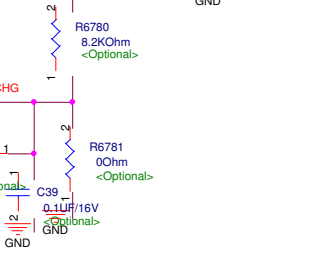
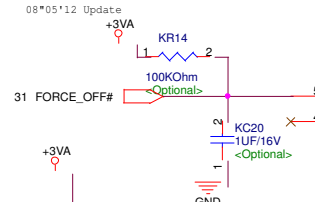
BOM OK.



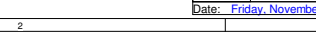
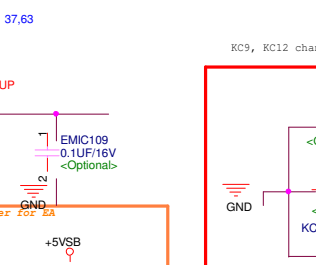
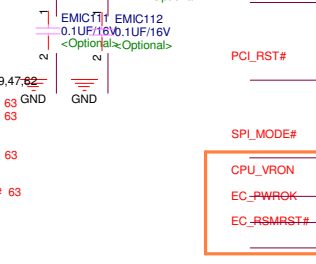
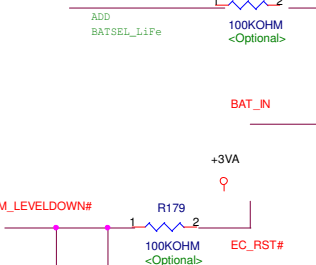
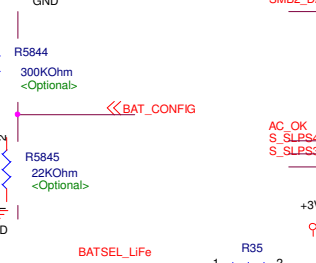
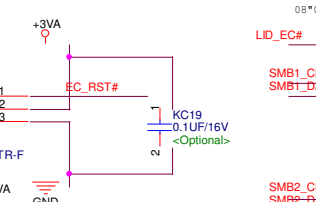
08\*04\*28 Update



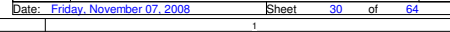
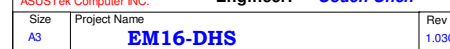
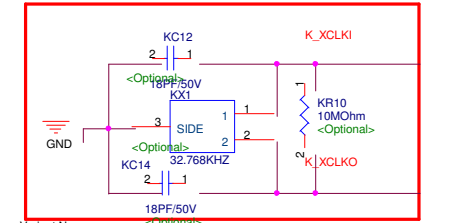
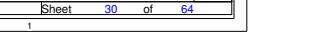
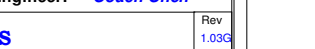
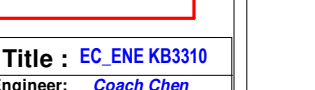
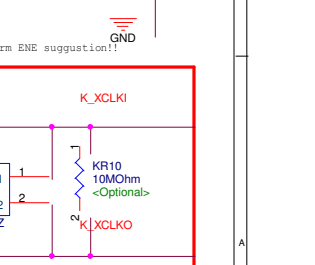
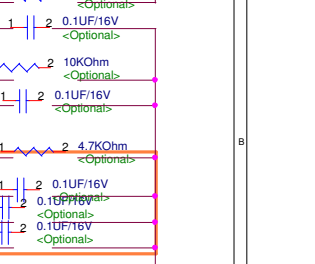
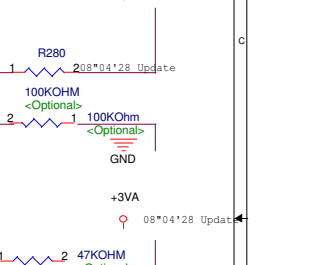
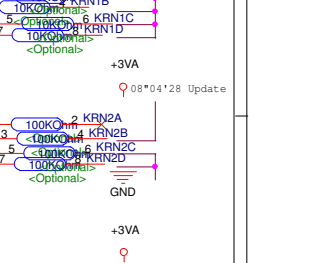
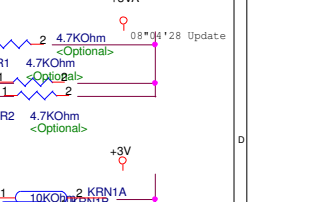
08\*04\*29 Update



08\*05\*12 Update



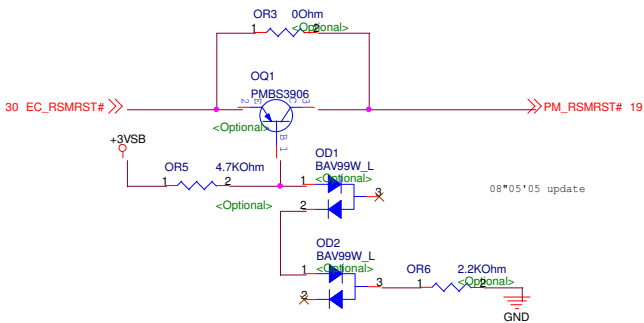
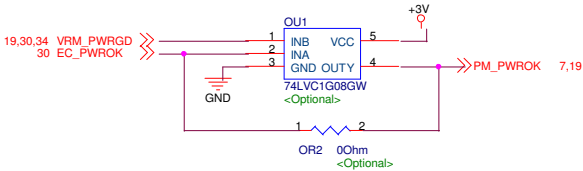
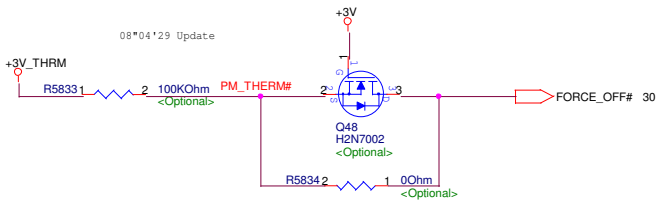
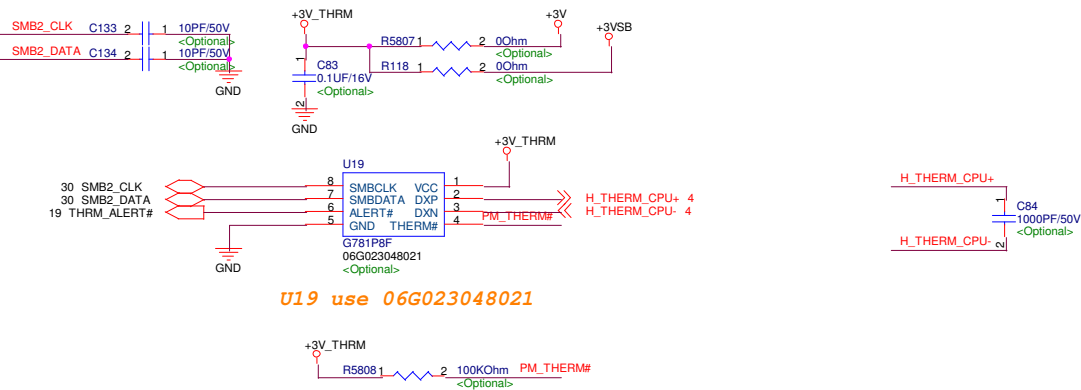
08\*04\*28 Update



ASUS Title : EC\_ENE KB3310  
ASUSTek Computer INC. Engineer: Coach Chen  
Size Project Name  
A3 EM16-DHS  
Date: Friday, November 07, 2008 Sheet 30 of 64

EC HW Monitor

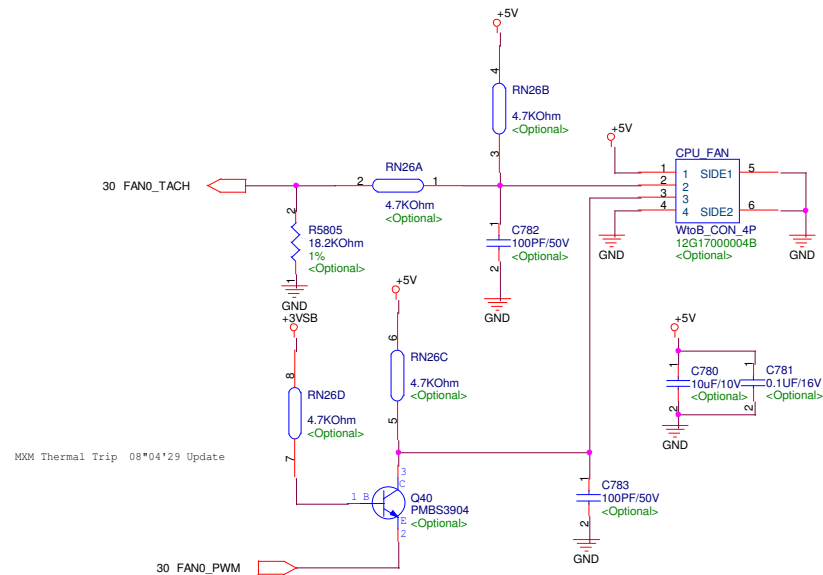
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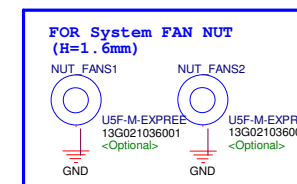
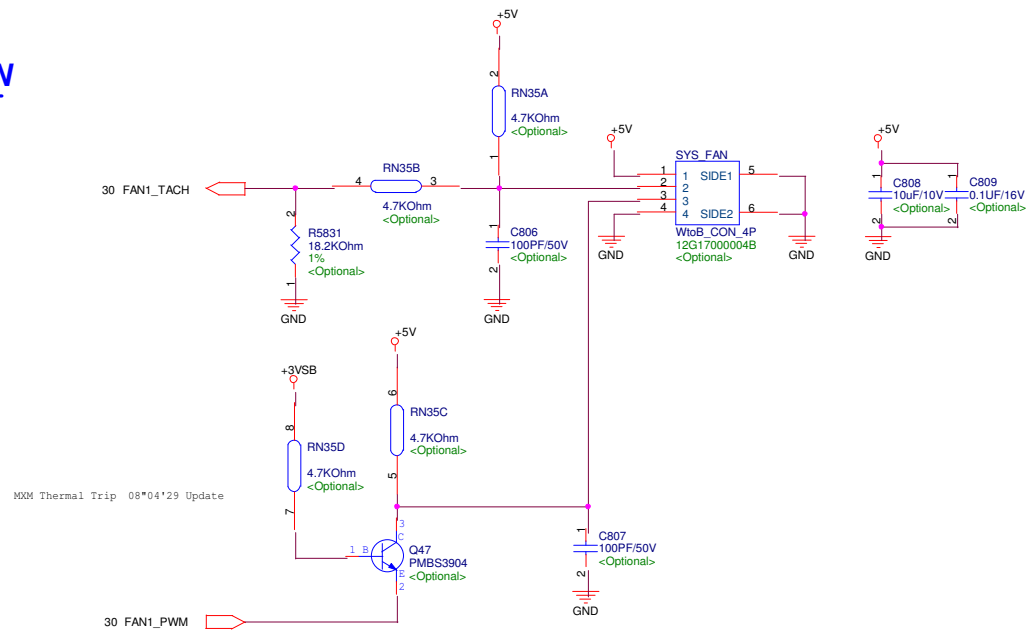
## CPU FAN

BOM OK.



## System FAN

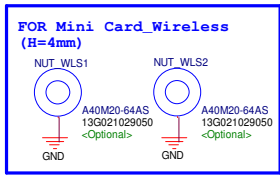
BOM OK.



<Variant Name>

<b>ASUS</b>		Title <b>EC FAN</b>	
ASUSTek Computer INC.		Engineer: <b>Coach Chen</b>	
Size A3	Project Name <b>EM16-DHS</b>	Rev 1.03G	
Date: Friday, November 07, 2008		Sheet 32	of 64

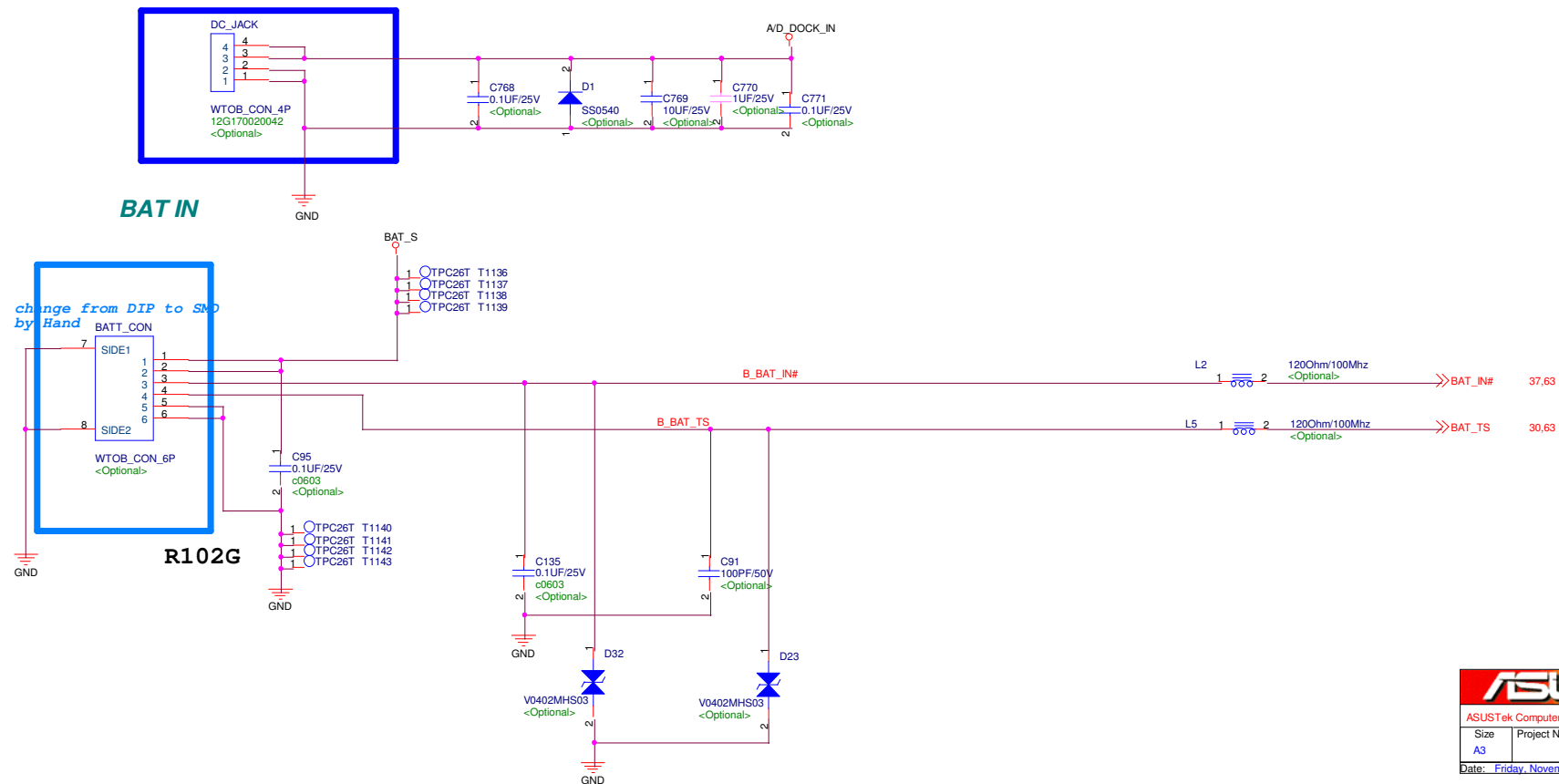
BOM OK.



20080508

## DC JACK

BOM OK.

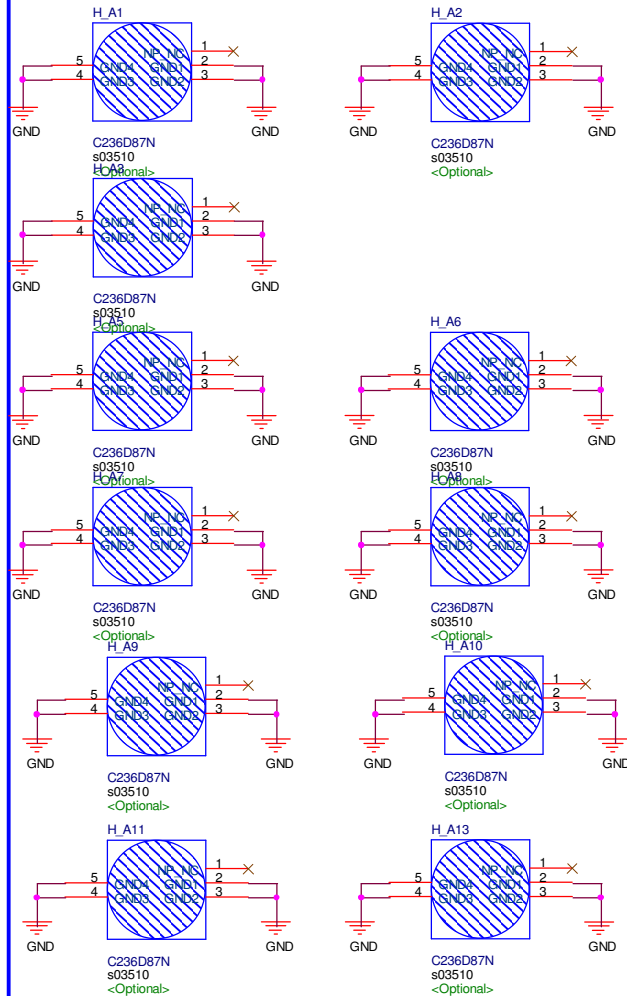


<b>ASUS</b>		Title : POWER SEQUENCE/ DC JACK	
ASUSTek Computer INC.		Engineer: Coach Chen	
Size A3	Project Name <b>EM16-DHS</b>		Rev 1.03G
Date: Friday, November 07, 2008		Sheet	40 of 64

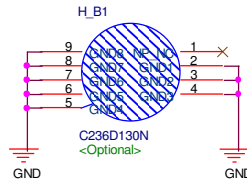
# SCREW HOLE

BOM OK.

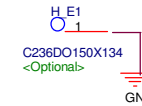
TYPE A (FOR MB) C=2.2mm / D=6.0mm



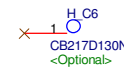
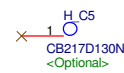
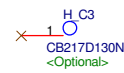
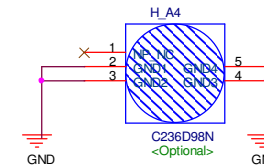
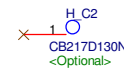
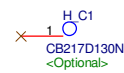
TYPE B (FOR MB)  
Left Coner  
C=3.4 mm / D=6.0mm



TYPE E (FOR MB)  
Right High Coner  
C=150\*134 mils / D=6.0mm

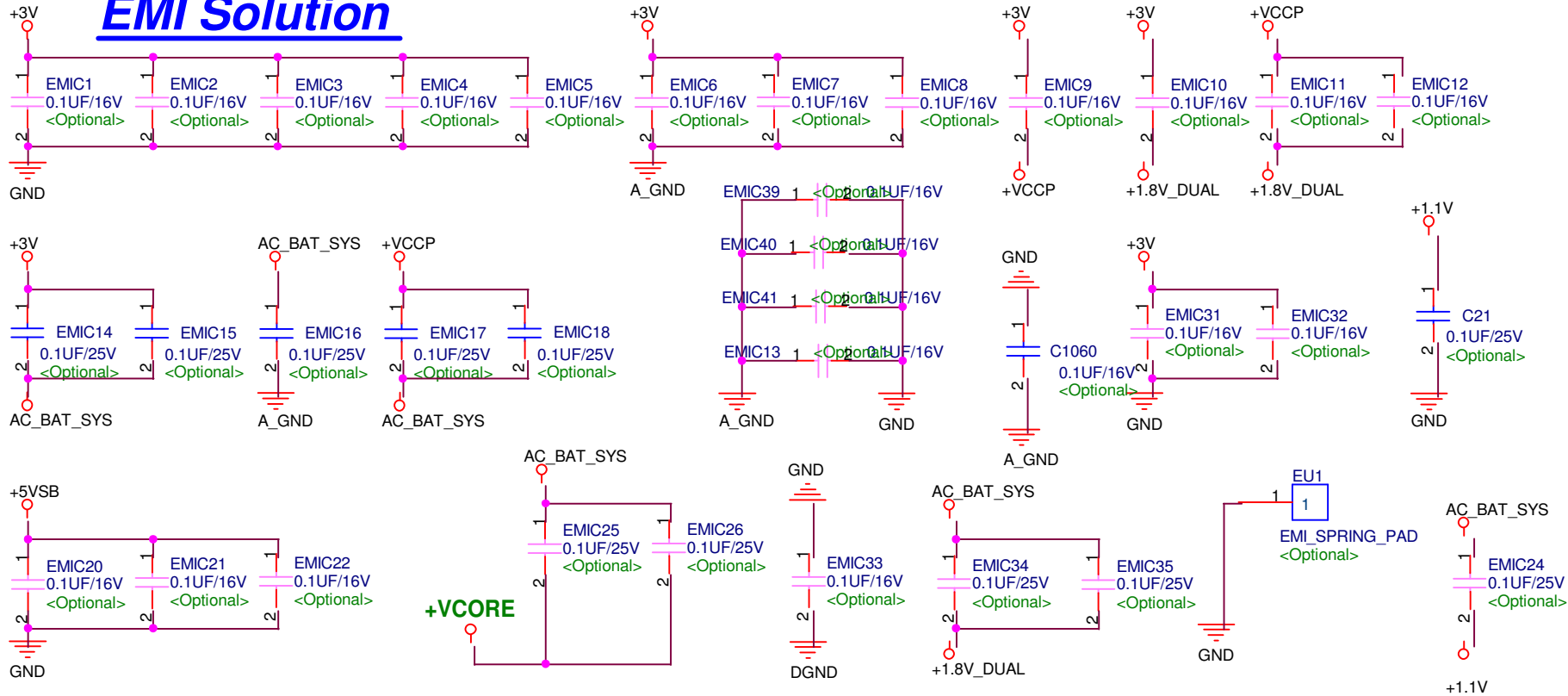


TYPE C (FOR CPU/NB) C=3.4 mm / D=5.5mm

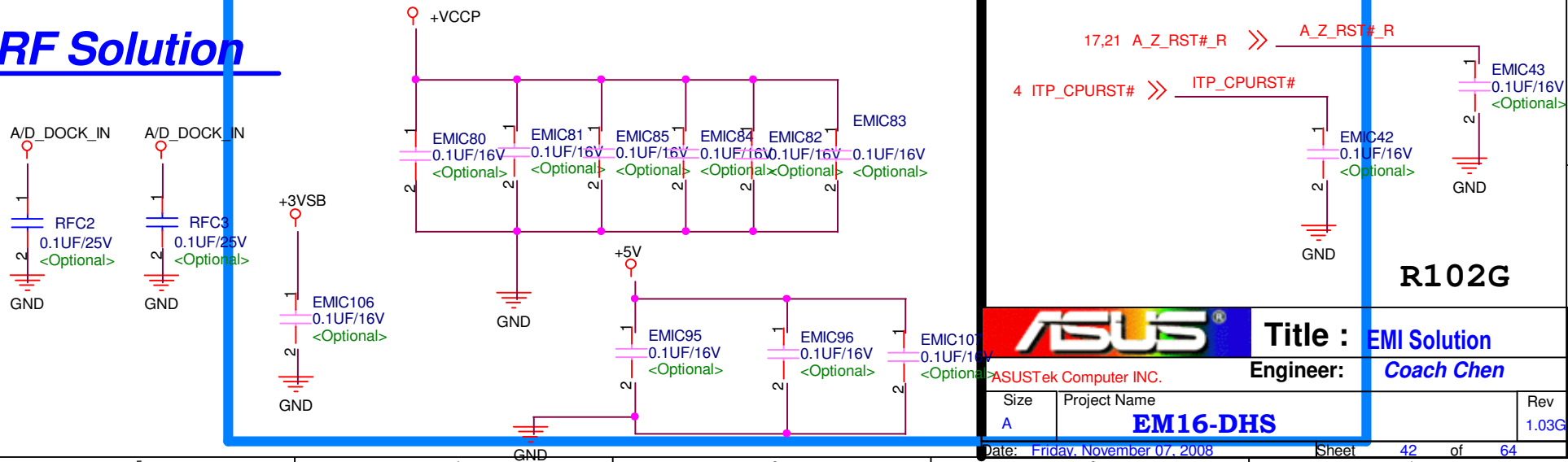


CHANGE SCREW HOLE

# EMI Solution



# RF Solution

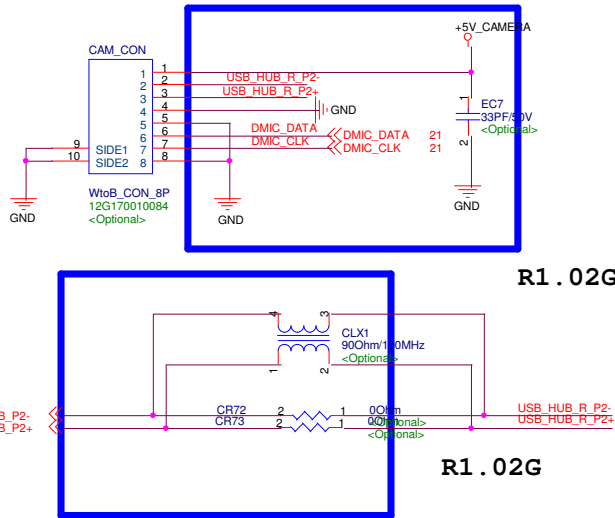
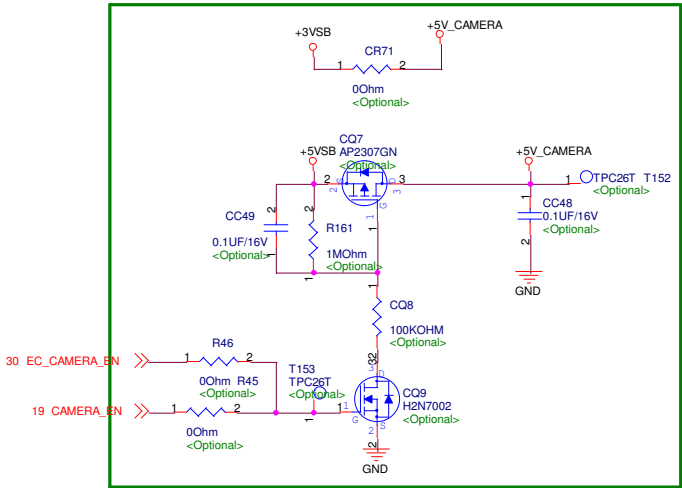


ASUS®		Title : NOS & AIG3(intel)	
ASUSTek Computer INC.		Engineer:Coach Chen	
Size A3	Project Name <b>EM16-DHS</b>	Rev 1.03G	
Date: Friday, November 07, 2008		Sheet 43	of 64

Camera Module

BOM OK.

Power Control

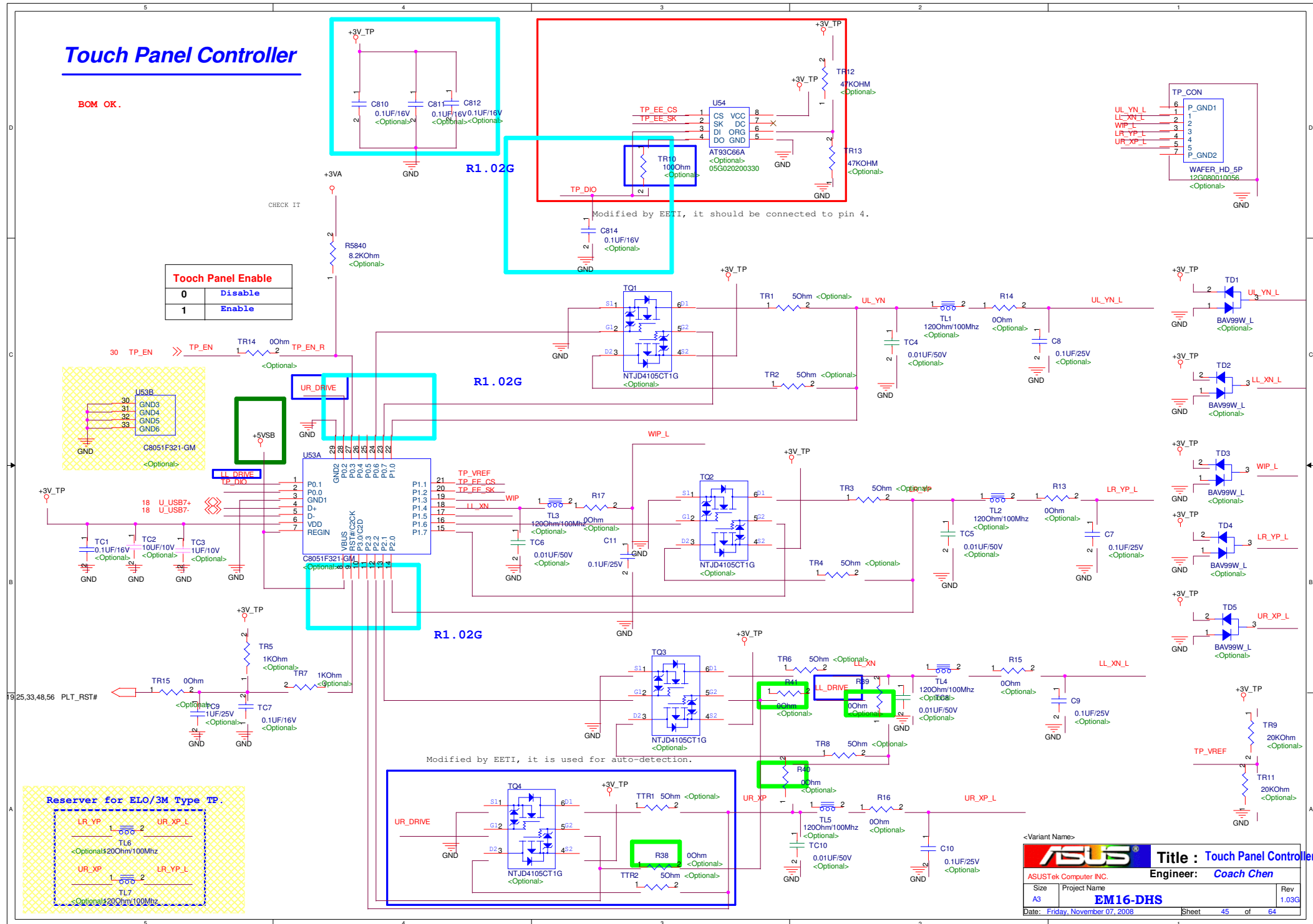




## ***Touch Panel Controller***

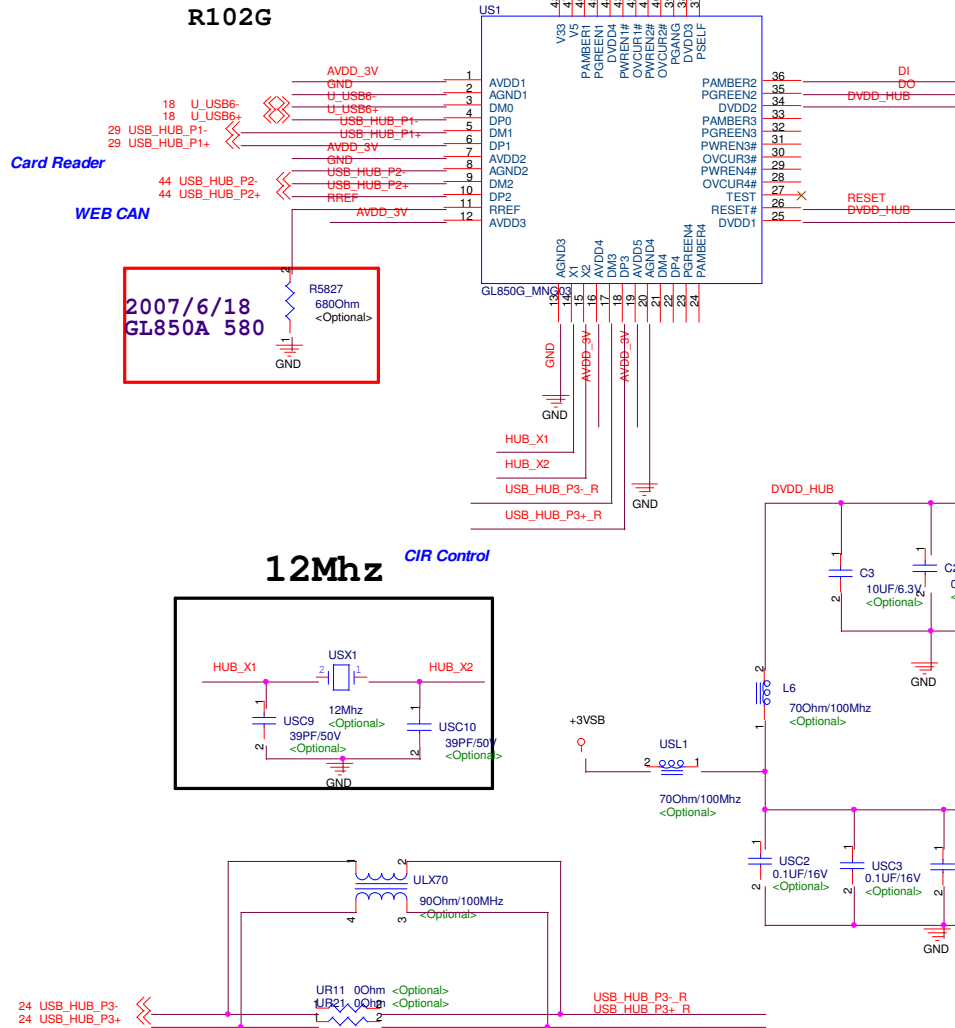
BOM OK.

Tooch Panel Enable	
0	Disable
1	Enable

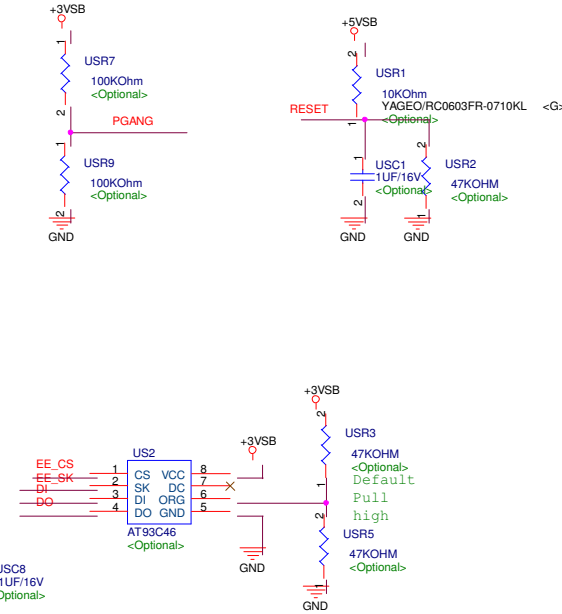


# USB HUB GL850A Config

DM/P0	SB USB Port 6 (USB Input)
DM/P1	Card Reader
DM/P2	Touch Panel Controller
DM/P3	CIR Control
DM/P4	NA

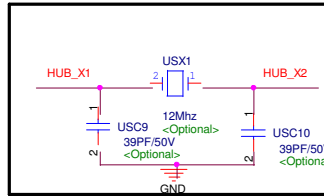


## 1: Power Self



## 12Mhz

CIR Control

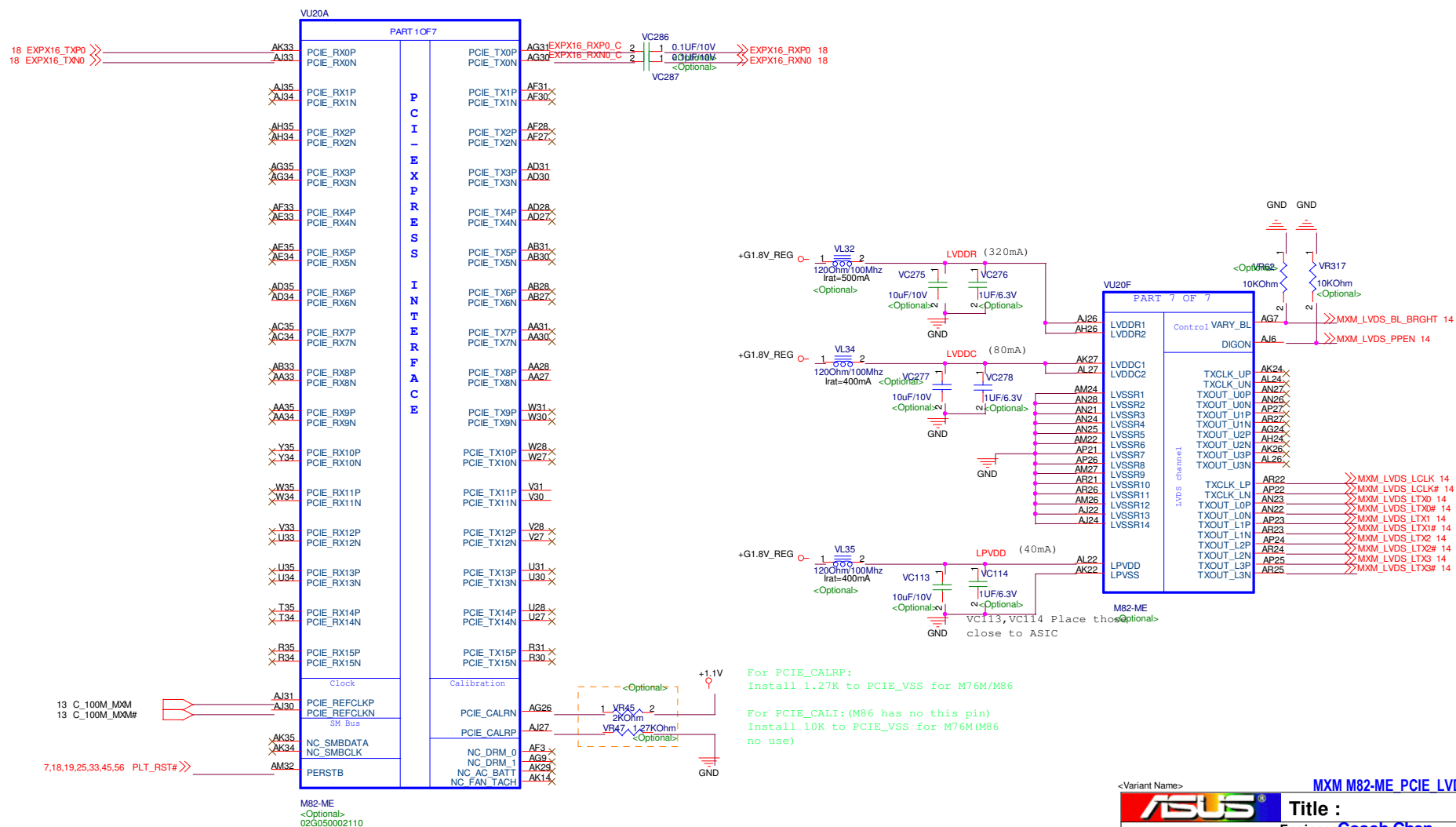


<Variant Name>

<b>ASUS</b>		Title : *	
ASUSTek Computer INC.		Engineer: Coach Chen	
Size	Project Name	Rev	
A3	EM16-DHS	1.03G	
Date: Friday, November 07, 2008		Sheet	46 of 64

## ***M82-ME\_PCIE\_LVDS***

BOM OK.



<Variant Name> **MXM M82-ME PCIE LVDS**

**ASUS** Title : **Engineer: Coach Chen**

ASUSTek Computer INC.

Size <b>A3</b>	Project Name <b>EM16-DHS</b>	Rev <b>1.030</b>
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Date: **Friday, November 07, 2008** Sheet **48** of **64**

BOM OK.



BOM OK.

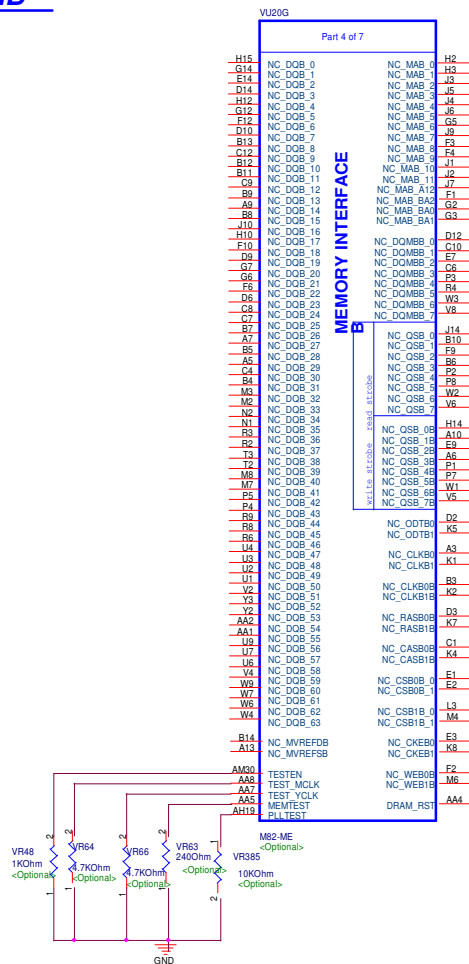


P/N: 03G15133F210



# M82-ME\_MEM CHB

BOM OK.

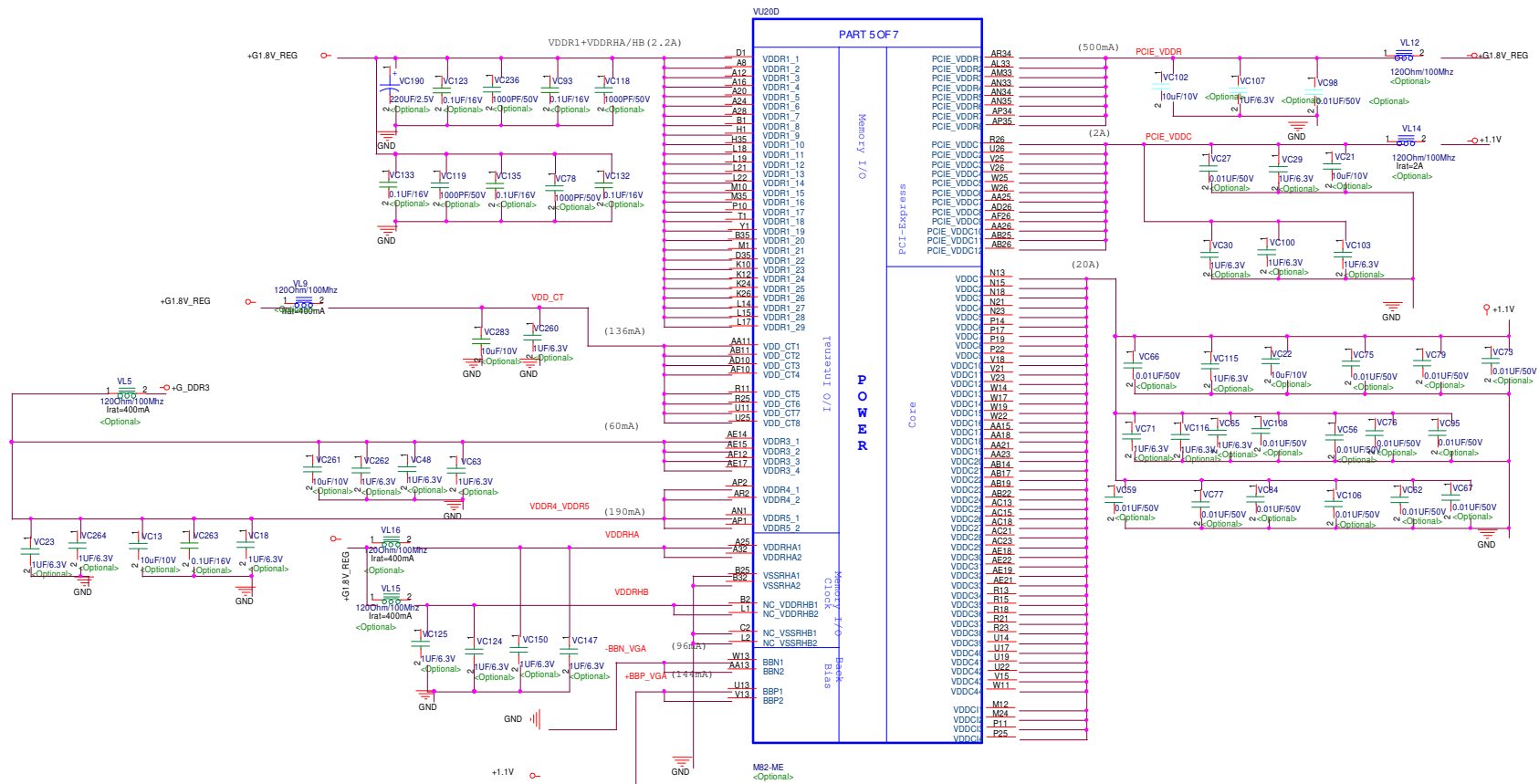


<Variant Name>

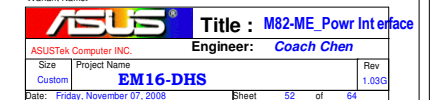
<b>ASUS</b>		<b>Title : M82-ME_MEM CHB</b>	
ASUSTek Computer INC.		Engineer: <b>Coach Chen</b>	
Size	Project Name	Rev	
Custom	<b>EM16-DHS</b>	1.03G	
Date: Friday, November 07, 2008	Sheet	51	of 64

### M82-ME\_Powr Interface

**BOM OK.**



<Variant Name>





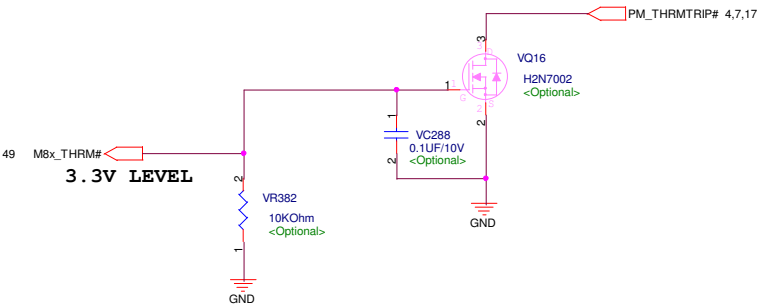
M82-ME\_GND Interface

BOM OK.

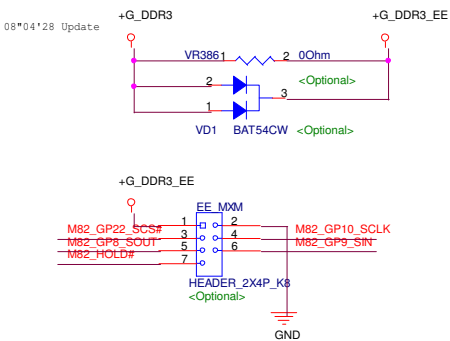
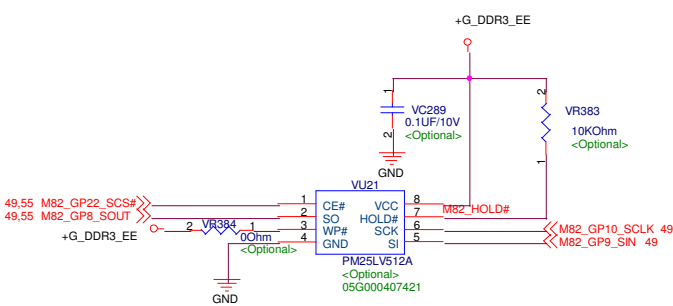
VU20E		
Part 6 of 7		
P33	PCIE_VSS1	VSS6
P34	PCIE_VSS2	VSS6
P35	PCIE_VSS2	M6
P27	PCIE_VSS3	VSS6
P28	PCIE_VSS4	K28
P29	PCIE_VSS5	VSS6
P32	PCIE_VSS6	M32
R33	PCIE_VSS7	N14
U29	PCIE_VSS8	VSS7
U32	PCIE_VSS9	N17
V29	PCIE_VSS10	N19
V32	PCIE_VSS11	N22
T33	PCIE_VSS12	VSS7
V34	PCIE_VSS13	N33
V35	PCIE_VSS14	N3
W29	PCIE_VSS15	VSS7
W32	PCIE_VSS16	R5
W33	PCIE_VSS17	VSS7
AA29	PCIE_VSS18	VSS7
AA32	PCIE_VSS19	VSS7
AB29	PCIE_VSS20	VSS8
AB32	PCIE_VSS21	VSS8
Y33	PCIE_VSS22	VSS8
AB34	PCIE_VSS23	VSS8
AB35	PCIE_VSS24	VSS8
AC33	PCIE_VSS25	VSS8
AD29	PCIE_VSS26	VSS8
AD32	PCIE_VSS27	VSS8
AF29	PCIE_VSS28	VSS8
AF32	PCIE_VSS29	VSS8
AD33	PCIE_VSS30	VSS8
AF34	PCIE_VSS31	VSS8
AF35	PCIE_VSS32	VSS8
AG27	PCIE_VSS33	VSS8
AG29	PCIE_VSS34	VSS8
AG32	PCIE_VSS35	VSS8
AG33	PCIE_VSS36	VSS8
AI29	PCIE_VSS37	VSS8
AI32	PCIE_VSS38	VSS8
AH33	PCIE_VSS39	VSS8
AL34	PCIE_VSS40	VSS8
AL35	PCIE_VSS41	VSS8
AK32	PCIE_VSS42	VSS8
AK33	PCIE_VSS43	VSS8
AK34	PCIE_VSS44	VSS8
A2	VSS1	VSS10
A34	VSS1	VSS10
C3	VSS2	VSS10
C6	VSS3	VSS10
A4	VSS4	VSS10
C18	VSS5	VSS10
A21	VSS6	VSS10
C23	VSS7	VSS10
C11	VSS8	VSS10
C13	VSS9	VSS10
C14	VSS10	VSS10
A18	VSS11	VSS10
A11	VSS12	VSS10
C26	VSS13	VSS10
C27	VSS14	VSS10
F35	VSS15	VSS10
R7	VSS16	VSS10
G10	VSS17	VSS10
F15	VSS18	VSS10
H17	VSS19	VSS10
G21	VSS20	VSS10
D29	VSS21	VSS10
A20	VSS22	VSS10
F11	VSS23	VSS10
F14	VSS24	VSS10
J15	VSS25	VSS10
E19	VSS26	VSS10
E22	VSS27	VSS10
F24	VSS28	VSS10
D7	VSS29	VSS10
F26	VSS30	VSS10
G29	VSS31	VSS10
D35	VSS32	VSS10
M3	VSS33	VSS10
G4	VSS34	VSS10
E10	VSS35	VSS10
E12	VSS36	VSS10
F17	VSS37	VSS10
G18	VSS38	VSS10
G22	VSS39	VSS10
F30	VSS40	VSS10
J35	VSS41	VSS10
H18	VSS42	VSS10
H19	VSS43	VSS10
J21	VSS44	VSS10
F7	VSS45	VSS10
J12	VSS46	VSS10
J24	VSS47	VSS10
J28	VSS48	VSS10
K30	VSS49	VSS10
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K45	VSS83	VSS10
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K124	VSS162	VSS10
K125	VSS163	VSS10
K126	VSS164	VSS10
K127	VSS165	VSS10
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K129	VSS167	VSS10
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K141	VSS179	VSS10
K142	VSS180	VSS10
K143	VSS181	VSS10
K144	VSS182	VSS10
K145	VSS183	VSS10
K146	VSS184	VSS10
K147	VSS185	VSS10
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K338	VSS376	VSS10
K339	VSS377	VSS10
K340	VSS378	VSS10
K341	VSS379	VSS10
K342	VSS380	VSS10
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K3		

M82\_ME THERMAL

BOM OK.



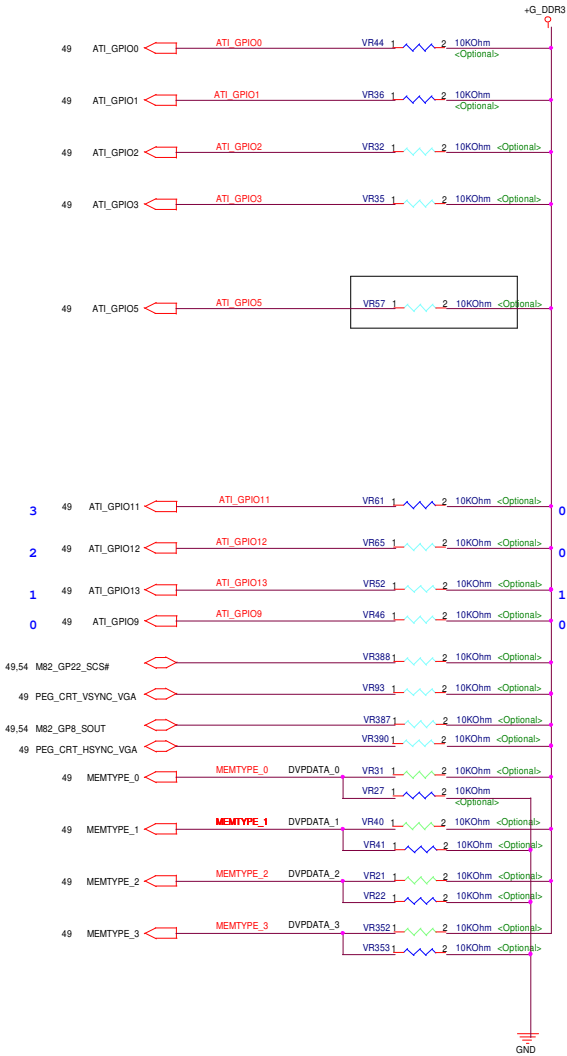
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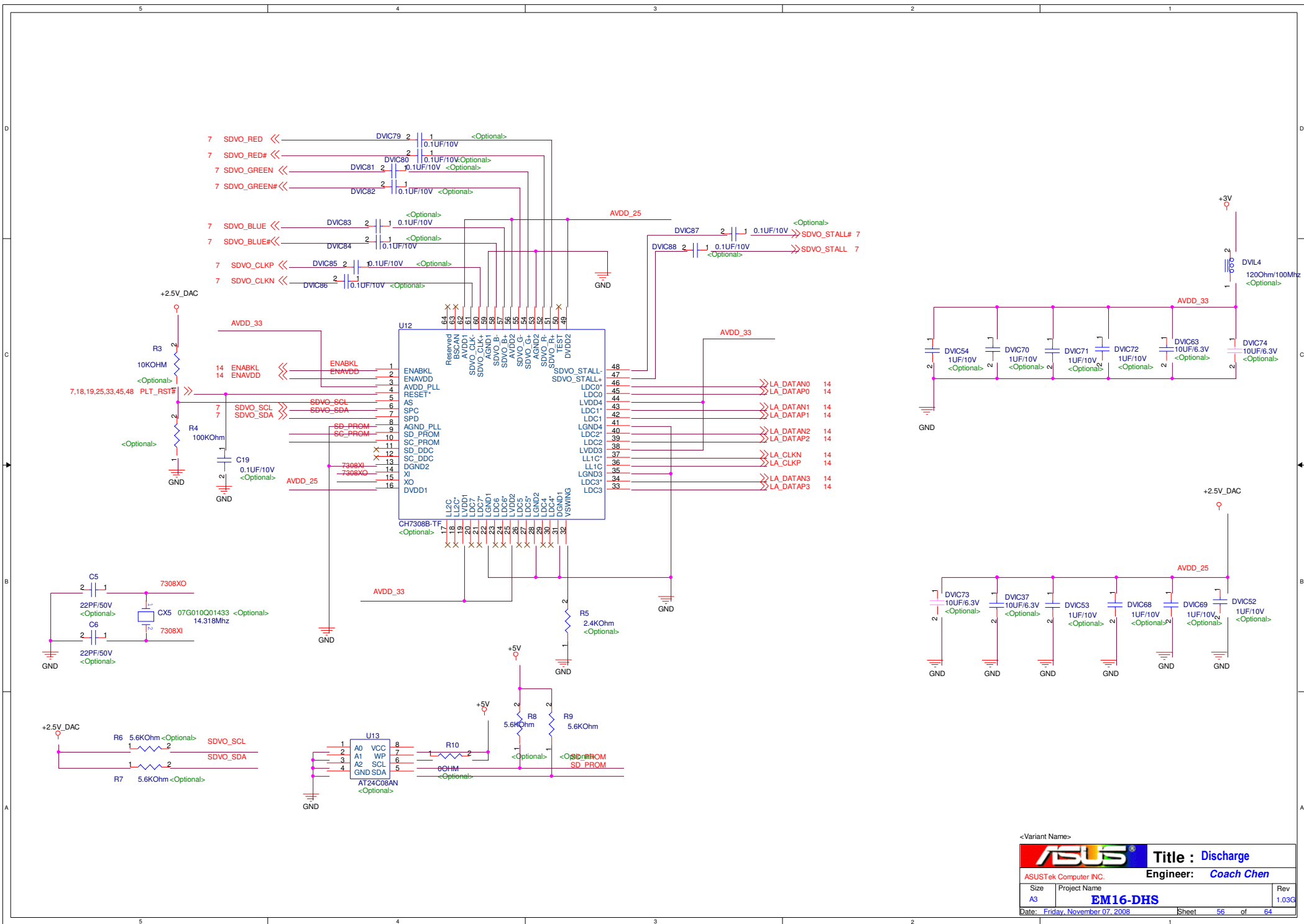


M82\_ME Straps

OPTION STRAPS

BOM OK.



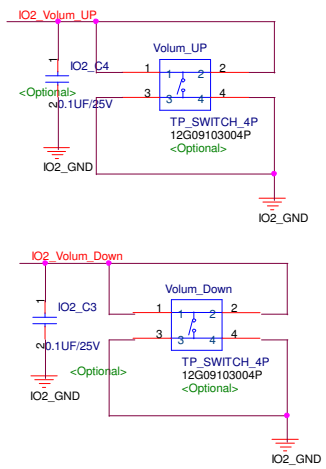


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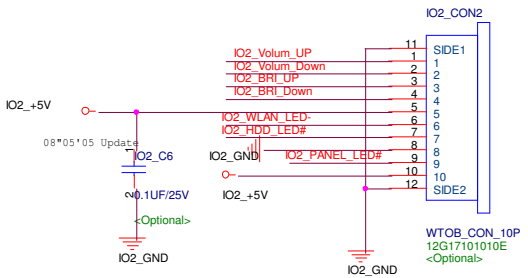
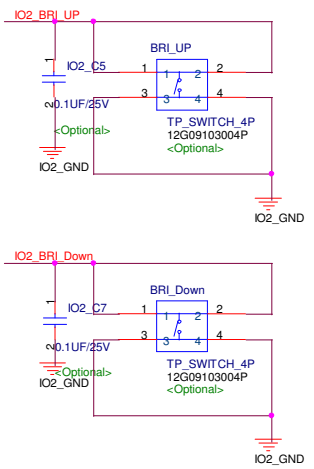
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ASUSTek Computer INC.		Engineer: <b>Coach Chen</b>	
Size <b>A3</b>	Project Name <b>EM16-DHS</b>	Rev <b>1.03G</b>	
Date: <b>Friday, November 07, 2008</b>		Sheet <b>56</b>	of <b>64</b>

EM16-IO2\_Function Brd

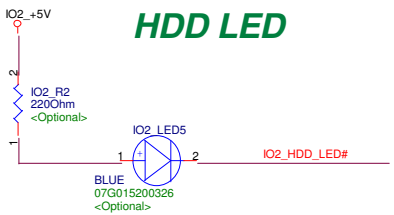
Volum Button



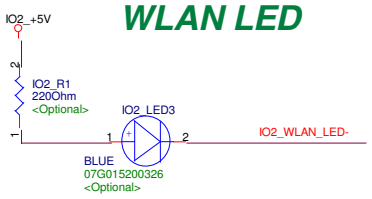
Brightness Button



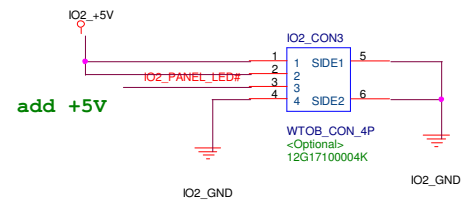
HDD LED



WLAN LED

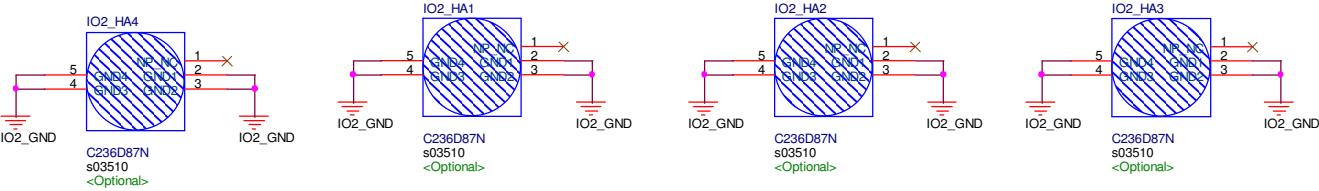


For Panel LED

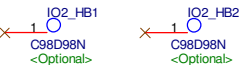


EM16-IO2 SCREW HOLE

TYPE A (FOR MB)  
C=2.2mm / D=6.0mm



TYPE B  
C=2.5mm NPH



TYPE C  
C=2.8mm NPH




<Variant Name>		Title : EM16-IO1 SCREW HOLE	
ASUSTek Computer INC.		Engineer: Coach Chen	
Size	Project Name	Rev	
B	EM16-DHS	1.03G	
Date: Friday, November 07, 2008		Sheet	58 of 64

ICH7-M GPIO SETTING



EC KB3310 GPIO SETTING

<Variant Name>



Title : EC KB3310 GPIO SETTING

ASUSTek Computer INC. Engineer: Coach Chen


Size	Project Name	Rev
A3	EM16-DHS	1.03G

Date: Friday, November 07, 2008 Sheet 60 of 64

# System Setting



<Variant Name>

		Title : System Setting	
ASUSTek Computer INC.		Engineer: Coach Chen	
Size	Project Name		Rev
A	EM16-DHS		1.03C
Date: Friday, November 07, 2008		Sheet	61 of 64

